

Compal Confidential

G470/G570 DIS+UMA+Muxless M/B Schematics Document

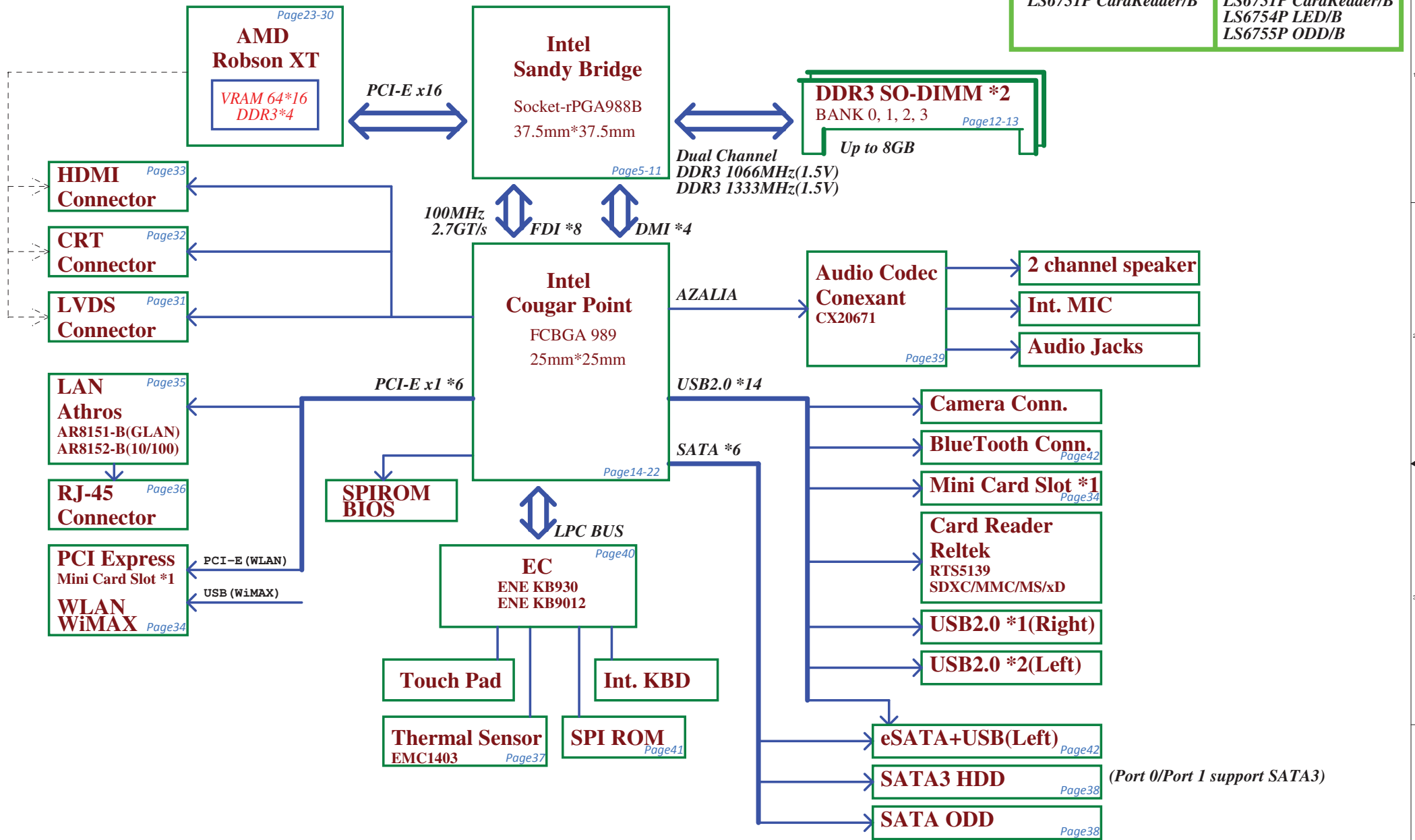
Intel Sandy Bridge Processor with DDRIII + Cougar Point PCH
ATI Robson/PX3.0,PX4.0

2010-10-22

LA-6751P / LA-6753P

REV:0.3

Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2010/07/12	Deciphered Date	2012/07/11	Title	
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(Port 0/Port 1 support SATA3)

power plane State	+B	+5VALW +3VALW	+1.5V	+5VS +3VS +1.5VS +VCCP +CPU_CORE +VGA_CORE +GFX_CORE +1.8VS +0.75VS +1.05VS
S0	O	O	O	O
S3	O	O	O	X
S5 S4/AC	O	O	X	X
S5 S4/ Battery only	O	X	X	X
S5 S4/AC & Battery don't exist	X	X	X	X

EC SM Bus2 address

Device	Device	Address
Smart Battery	0001 011X b	Thermal Sensor EMC1403-2
		1001_101xb
		Thermal Sensor EMC1402-1
		100_1100 b

Device	Address
DDR DIMM0	1001 000Xb
DDR DIMM2	1001 010Xb

	SOURCE	VGA	BATT	KE930	SODIMM	WLAN WWAN	Thermal Sensor	PCH
SMB_EC_CK1 SMB_EC_DA1	KB930 +3VALW	X	V +3VALW	X	X	X	X	X
SMB_EC_CK2 SMB_EC_DA2	KB930 +3VALW	X	X	X	X	X	X	V +3VS
SMBCLK SMBDATA	PCH +3VALW	X	X	X	V +3VS	V +3VS	X	X
SML0CLK SML0DATA	PCH +3VALW	X	X	X	X	X	X	X
SML1CLK SML1DATA	PCH +3VALW	V +3VS	X	V +3VS	X	X	V +3VS	X

STATE \ SIGNAL	SLP_S1#	SLP_S3#	SLP_S4#	SLP_S5#	+VALW	+V	+VS	Clock
Full ON	HIGH	HIGH	HIGH	HIGH	ON	ON	ON	ON
S1 (Power On Suspend)	LOW	HIGH	HIGH	HIGH	ON	ON	ON	LOW
S3 (Suspend to RAM)	LOW	LOW	HIGH	HIGH	ON	ON	OFF	OFF
S4 (Suspend to Disk)	LOW	LOW	LOW	HIGH	ON	OFF	OFF	OFF
S5 (Soft OFF)	LOW	LOW	LOW	LOW	ON	OFF	OFF	OFF

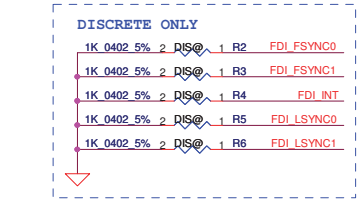
Board ID	PCB Revision
0	0.1
1	
2	
3	
4	
5	
6	
7	

Vcc	3.3V +/- 5%				
Ra/Rc/Re	100K +/- 5%				
Board ID	Rb / Rd / Rf	VAD_BID min	VAD_BID typ	VAD_BID max	
0	0	0 V	0 V	0 V	EVT
1	8.2K +/- 5%	0.216 V	0.250 V	0.289 V	DVT
2	18K +/- 5%	0.436 V	0.503 V	0.538 V	PVT
3	33K +/- 5%	0.712 V	0.819 V	0.875 V	MP
4	56K +/- 5%	1.036 V	1.185 V	1.264 V	
5	100K +/- 5%	1.453 V	1.650 V	1.759 V	
6	200K +/- 5%	1.935 V	2.200 V	2.341 V	
7	NC	2.500 V	3.300 V	3.300 V	

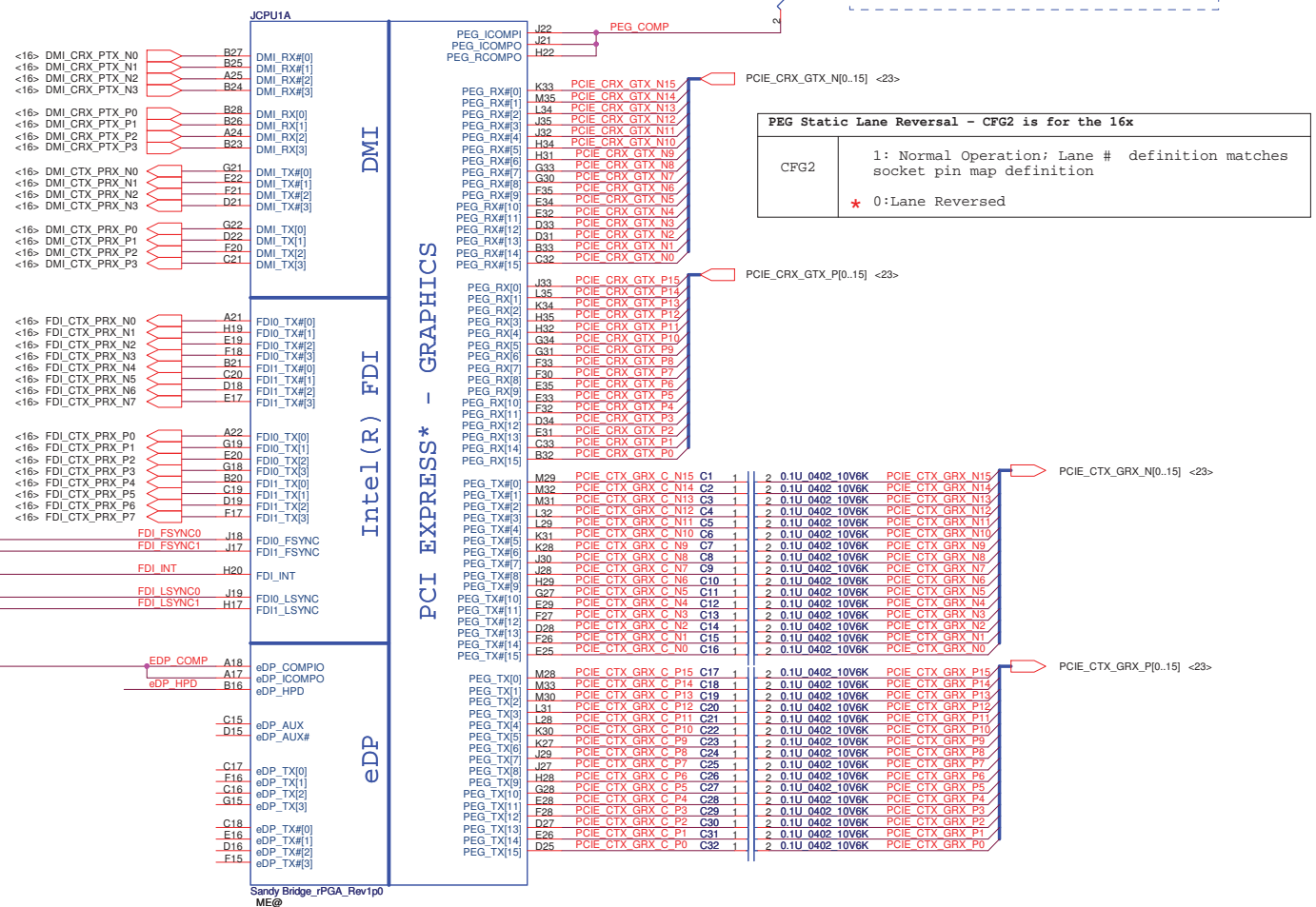
USB 2.0	USB 1.1	Port	3 External USB Port
EHCI1	UHCI0	0	USB/B (Right Side)
		1	USB Port (Left Side)
	UHCI1	2	USB Port (Left Side)
		3	USB Port (Left Side)
	UHCI2	4	
		5	Camera
	UHCI3	6	
		7	
EHCI2	UHCI4	8	Mini Card(WLAN)
		9	
	UHCI5	10	
		11	Card Reader
	UHCI6	12	
		13	Blue Tooth

BTO Item	BOM Structure
UMA and PX bus	PX@
Discrete Only	DIS@
PX3.0 only, not for BACO	PX3@
BACO	BACO@
COMMON HDMI	HDMI@
UMA HDMI	UMA_HDMI@
Discrete HDMI	VGA_HDMI@
eSATA	ESATA@
Blue Tooth	BT@
Connector	ME@
45 LEVEL	45@
10/100 LAN	8152@
GIGA LAN	GIGA@
Camera	CMOS@
Unpop	@

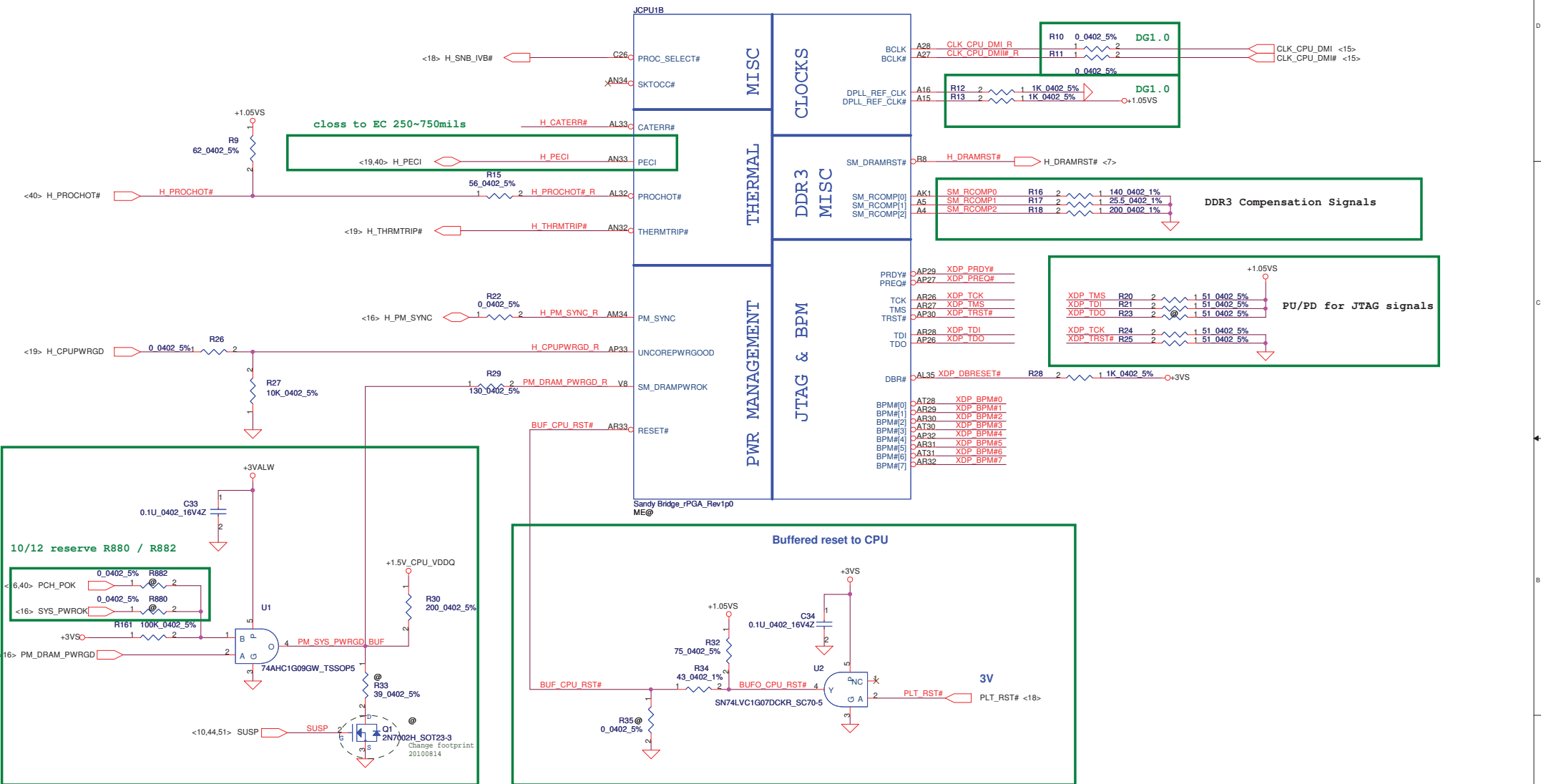
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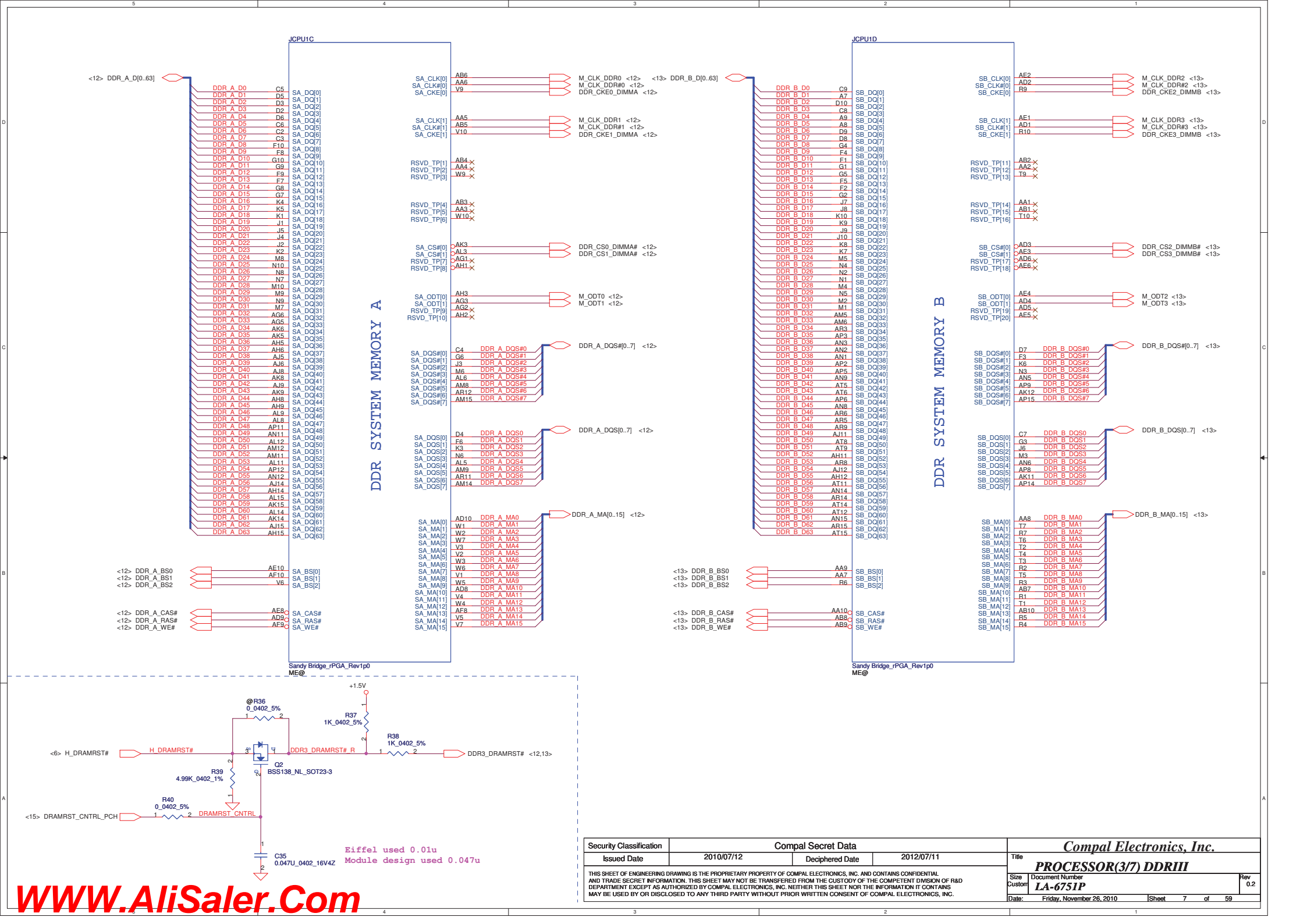


eDP_COMPIO and ICOMPO signals should be shorted near balls and routed with typical impedance <25 mohms

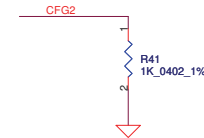
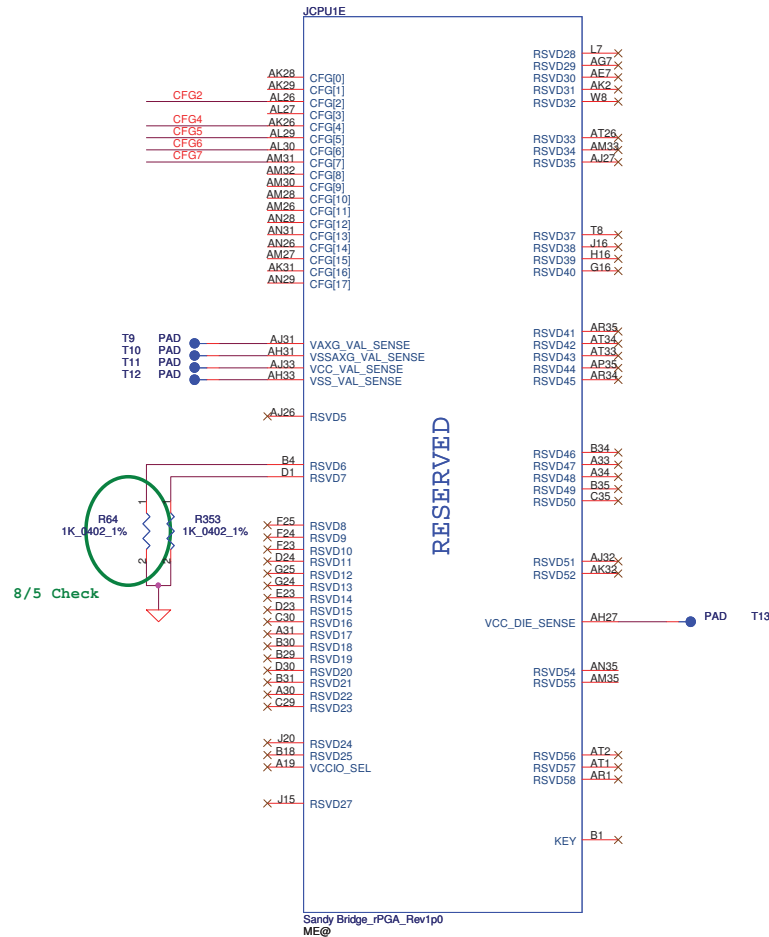


PEG_ICOMPI and RCOMPO signals should be shorted and routed with - max length = 500 mils - typical impedance = 43 mohms
PEG_ICOMPO signals should be routed with - max length = 500 mils - typical impedance = 14.5 mohms

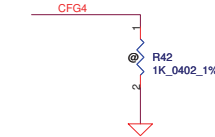




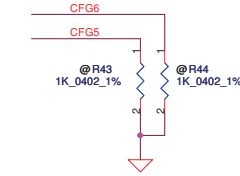
CFG Straps for Processor



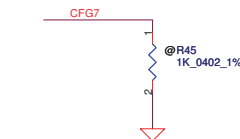
PEG Static Lane Reversal - CFG2 is for the 16x	
CFG2	1: Normal Operation; Lane # definition matches socket pin map definition * 0: Lane Reversed



Display Port Presence Strap	
CFG4	* 1 : Disabled; No Physical Display Port attached to Embedded Display Port 0 : Enabled; An external Display Port device is connected to the Embedded Display Port



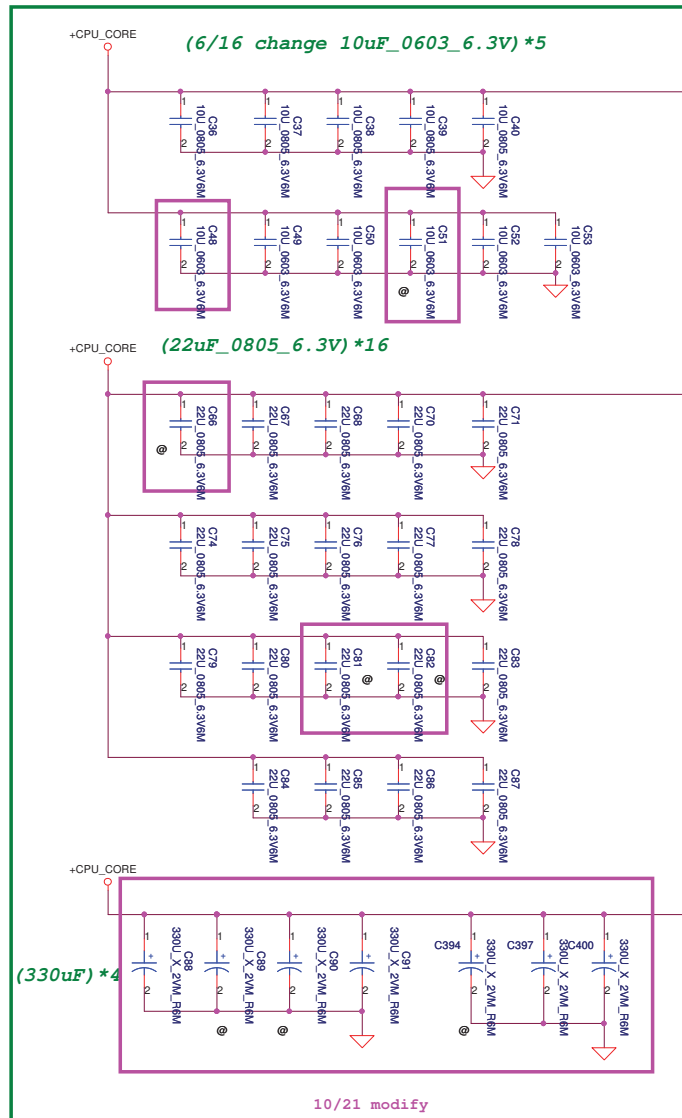
PCIe Port Bifurcation Straps	
CFG[6:5]	* 11: (Default) x16 - Device 1 functions 1 and 2 disabled 10: x8, x8 - Device 1 function 1 enabled ; function 2 disabled 01: Reserved - (Device 1 function 1 disabled ; function 2 enabled) 00: x8,x4,x4 - Device 1 functions 1 and 2 enabled



PEG DEFER TRAINING	
CFG7	1: (Default) PEG Train immediately following xxRESETB de assertion 0: PEG Wait for BIOS for training

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POWER



10/21 modify

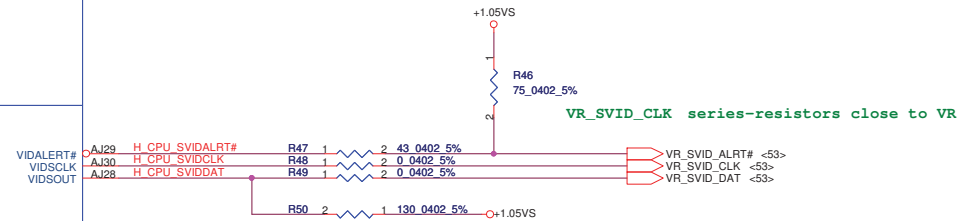
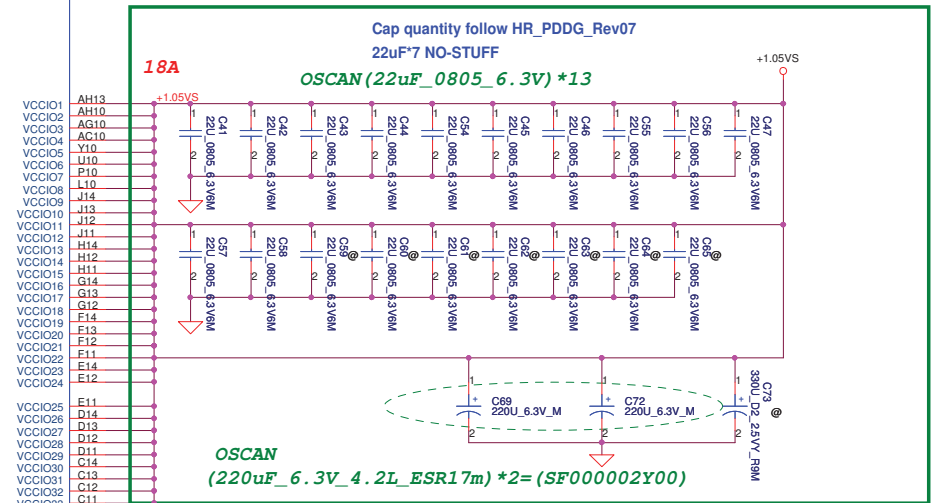
$$\begin{aligned} QC &= 94A \\ DC &= 53A \end{aligned}$$
$$\begin{aligned} QC &= 94A \\ DC &= 53A \end{aligned}$$

JCPU1F

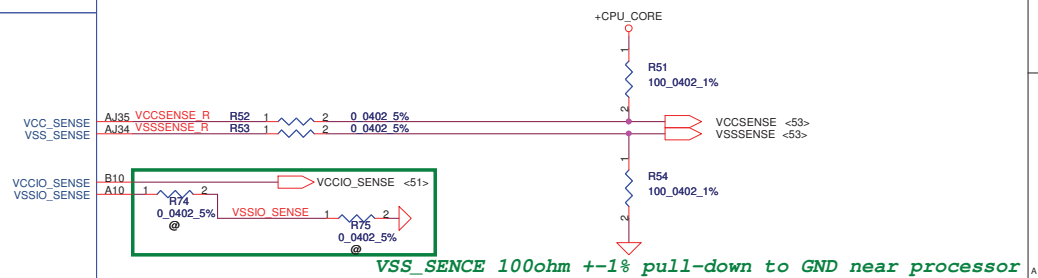
CORE SUPPLY

SVTD

SENSE, TIMES

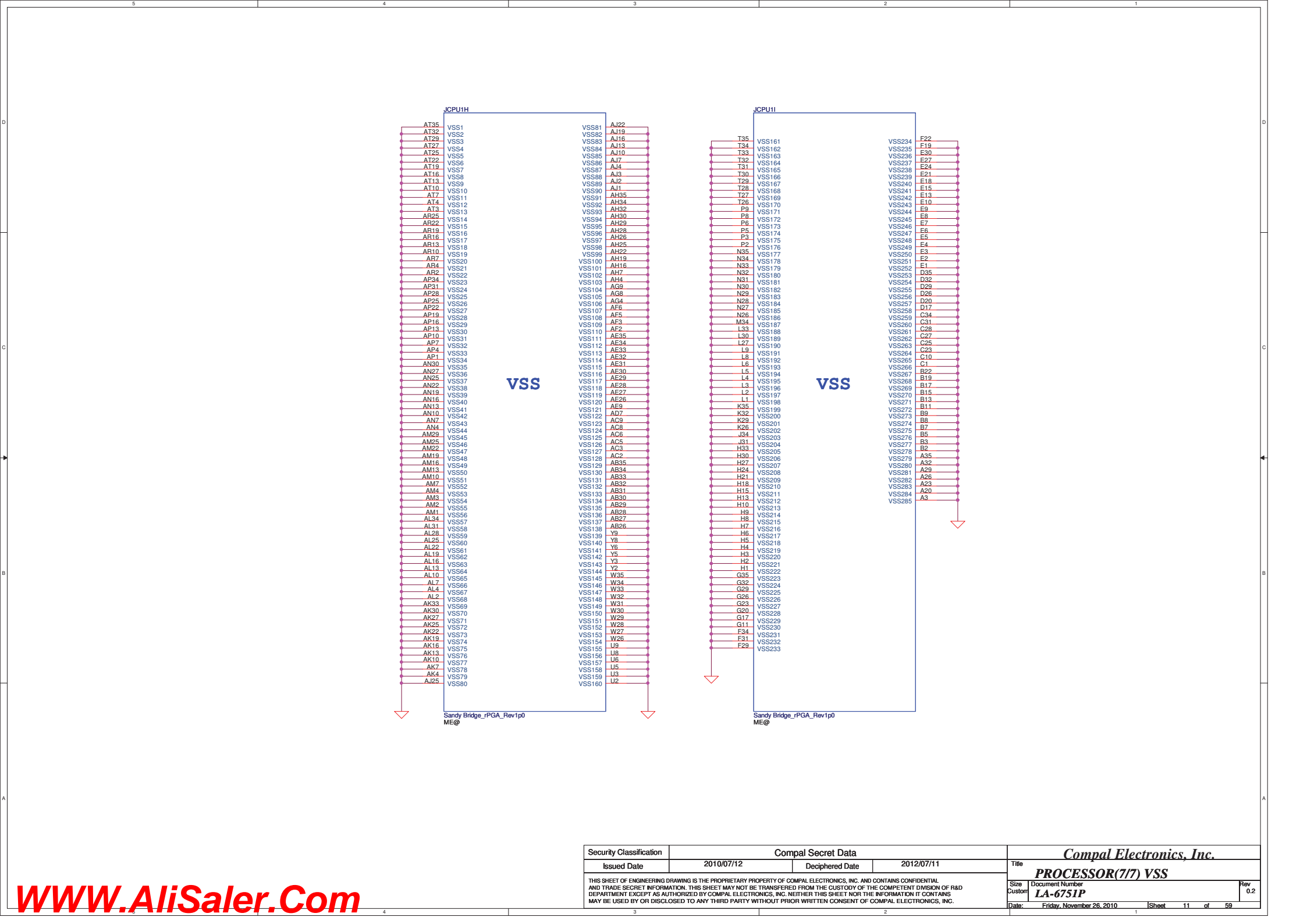


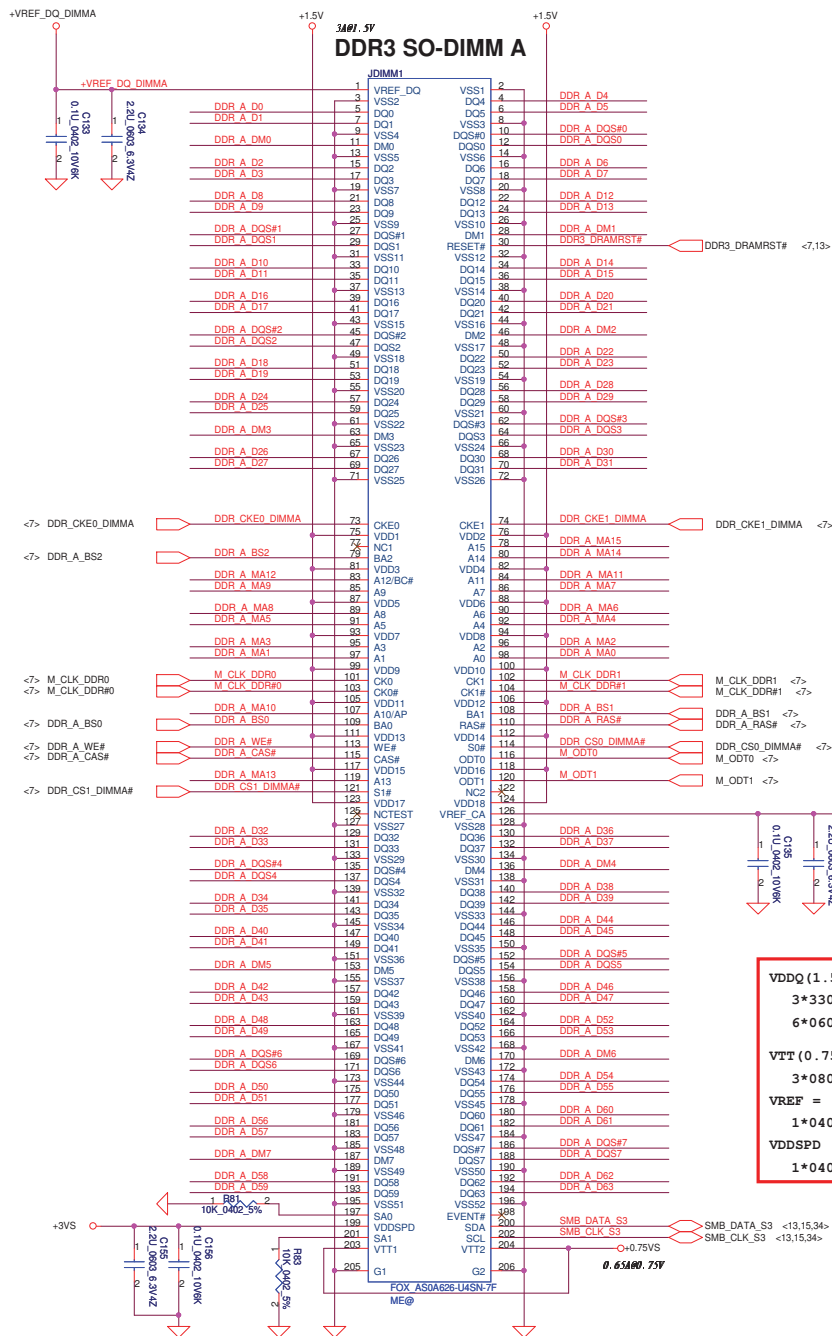
VCC_SENCE 100ohm $\pm 1\%$ pull-up to VCC near processor



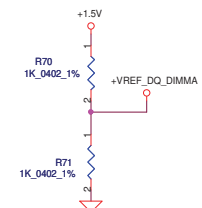
8/12 Modify, need follow differential routing
R74 close CPU, R75 close PWR

Security Classification	Compal Secret Data			<i>Compal Electronics, Inc.</i> PROCESSOR(5/7) PWR,BYPASS		
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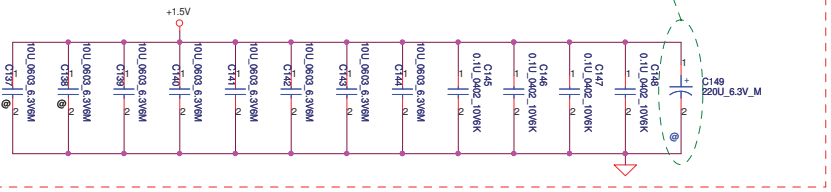




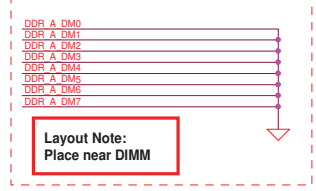
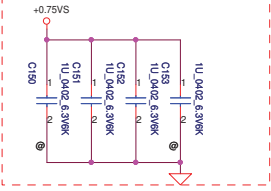
<7> DDR_A_D[0..63]
<7> DDR_A_DQS[0..7]
<7> DDR_A_DQS#0..7
<7> DDR_A_MA[0..15]



Layout Note:
Place near DIMM



Layout Note:
Place near DIMM



VDDQ(1.5V) =
3*330uf / 12m ohm (TOTAL FOR 2 SO-DIMMs)
6*0603 10uf (PER CONNECTOR)

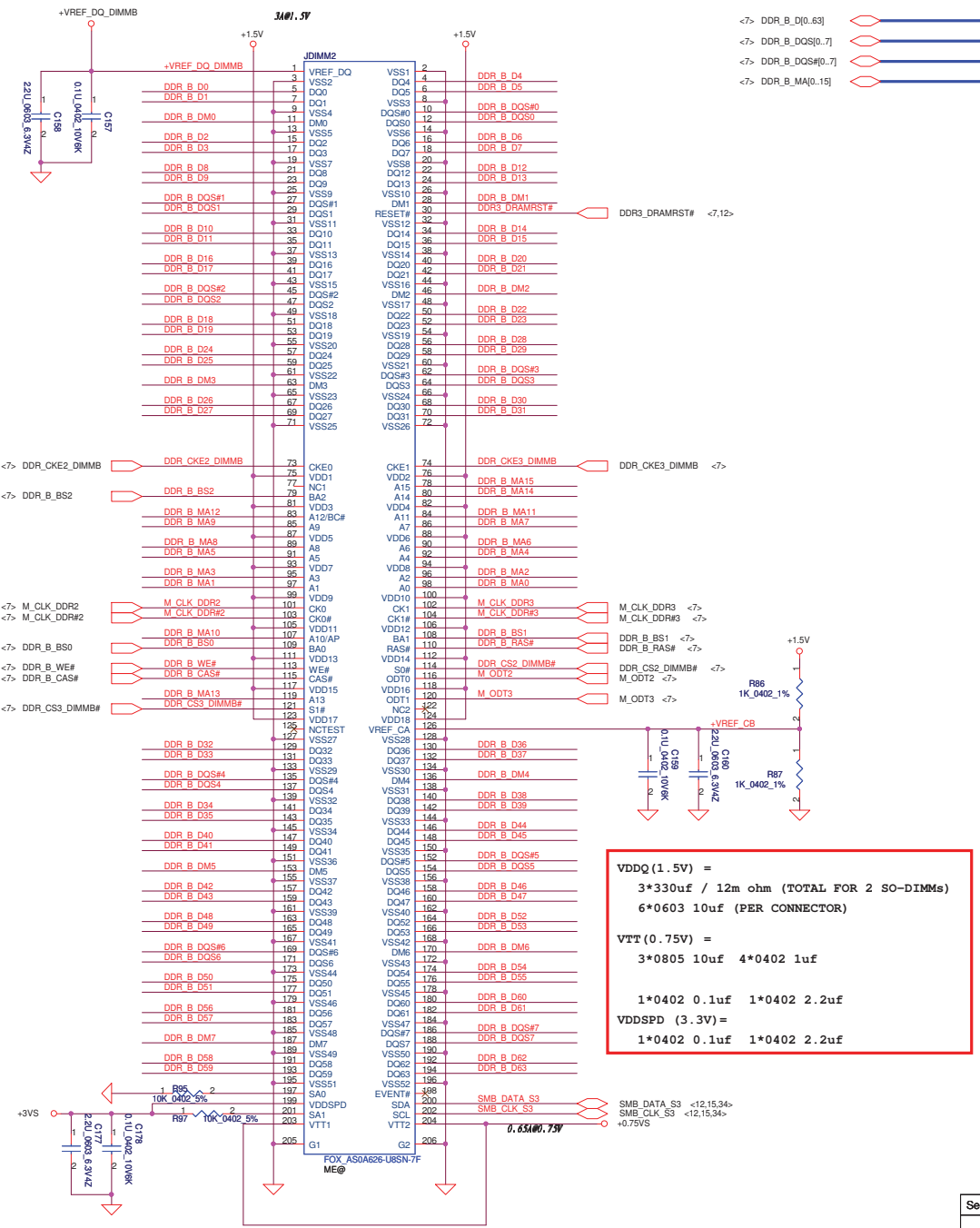
VTT(0.75V) =
3*0805 10uf 4*0402 1uf

VREF =
1*0402 0.1uf 1*0402 2.2uf

VDDSPD (3.3V) =
1*0402 0.1uf 1*0402 2.2uf

OSCAN (220uF_6.3V_4.2L_ESR17m)*1=(SF000002Y00)
(10uF_0603_6.3V)*8
(0.1uF_402_10V)*4

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For Arranale only +VREF_DQ_DIMMB supply from a external 1.5V voltage divide circuit.

07/17/2009

Layout Note:
Place near DIMM

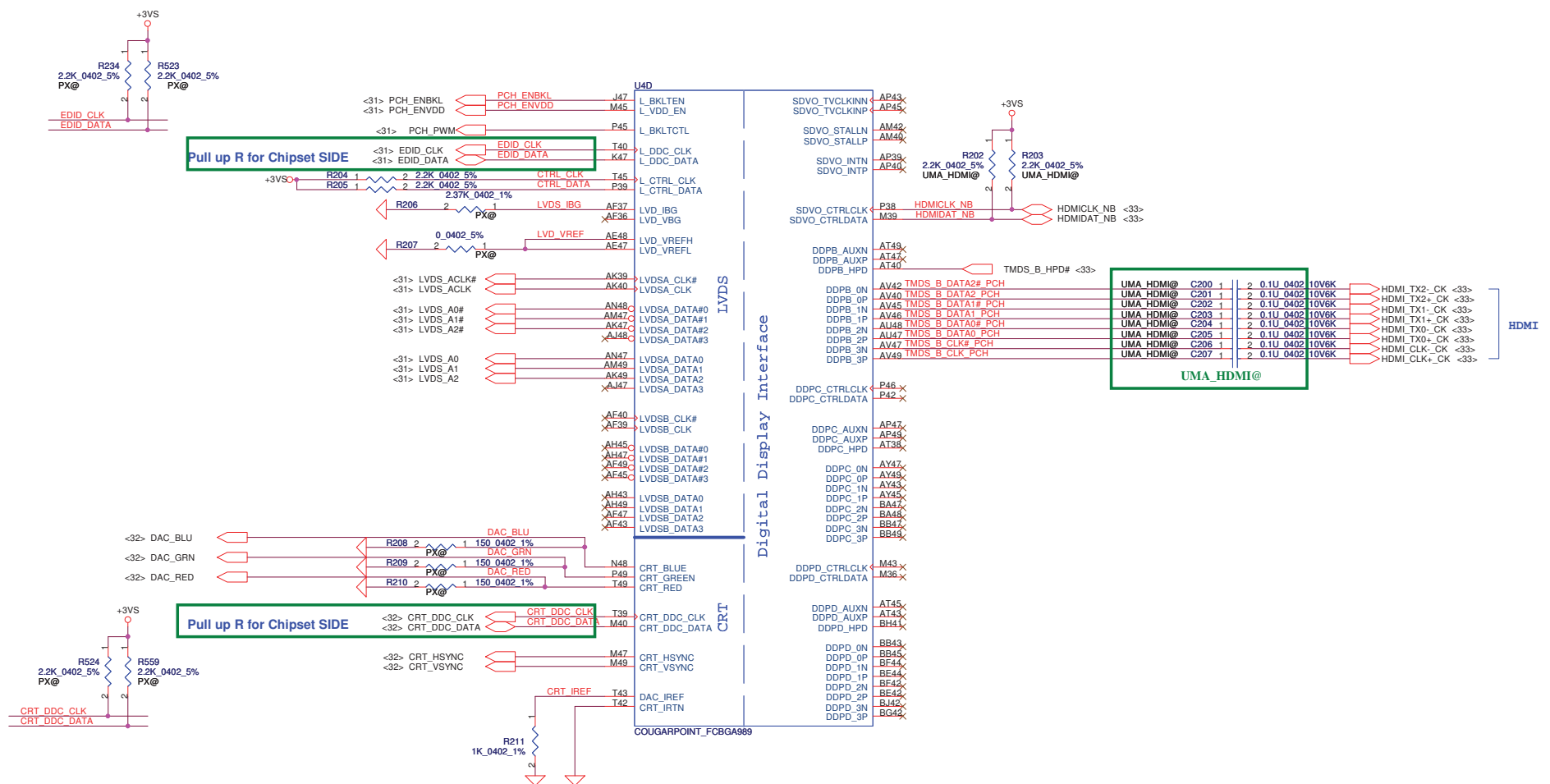
(10uF_0603_6.3V)*8
(0.1uF_402_10V)*4

Layout Note:
Place near DIMM

7/28 Update connect GND directly

Layout Note:
Place near DIMM

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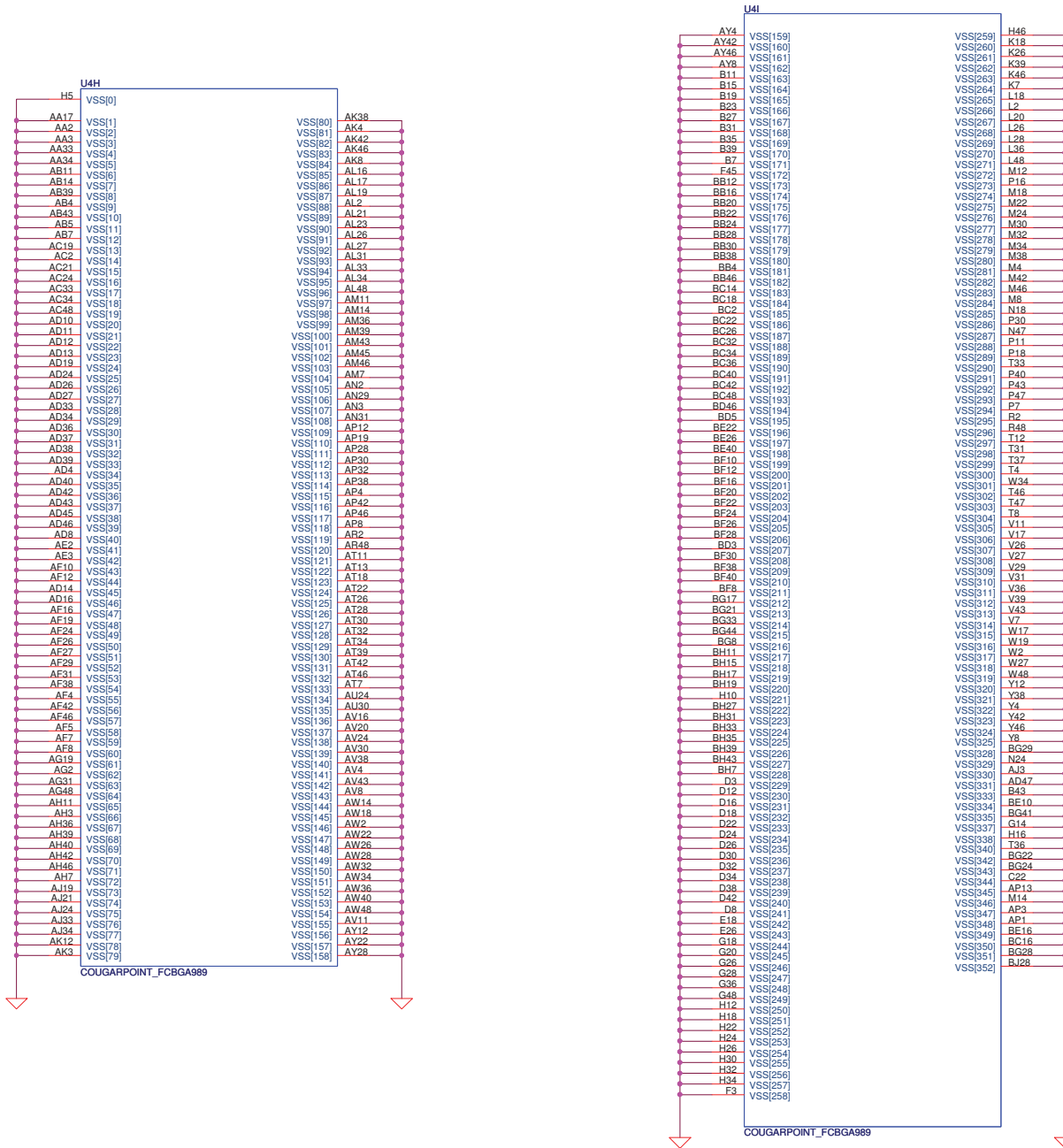


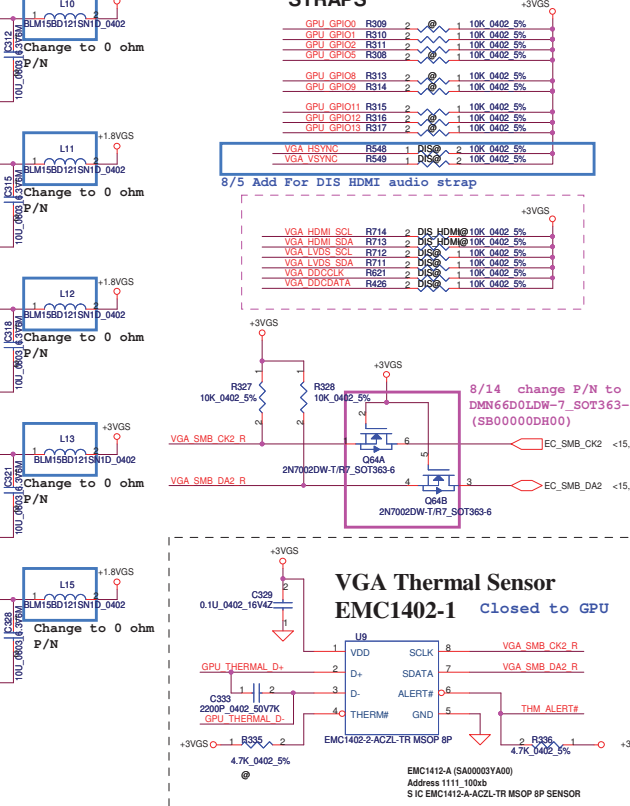
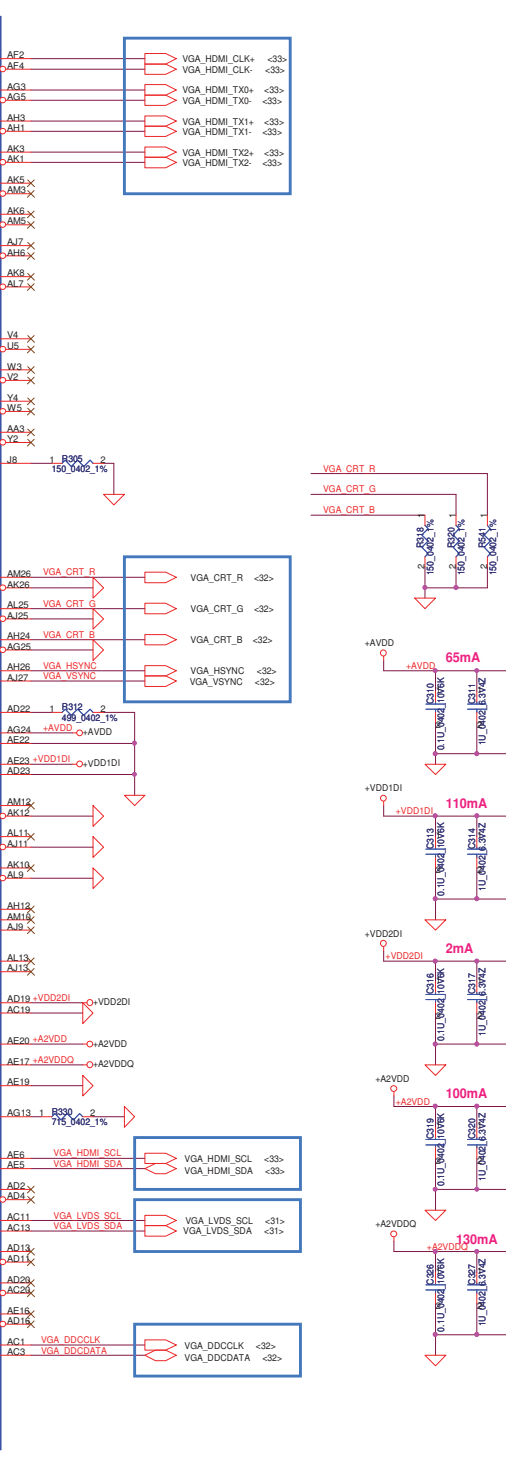
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				PCH (4/9) LVDS,CRT,DP,HDMI	
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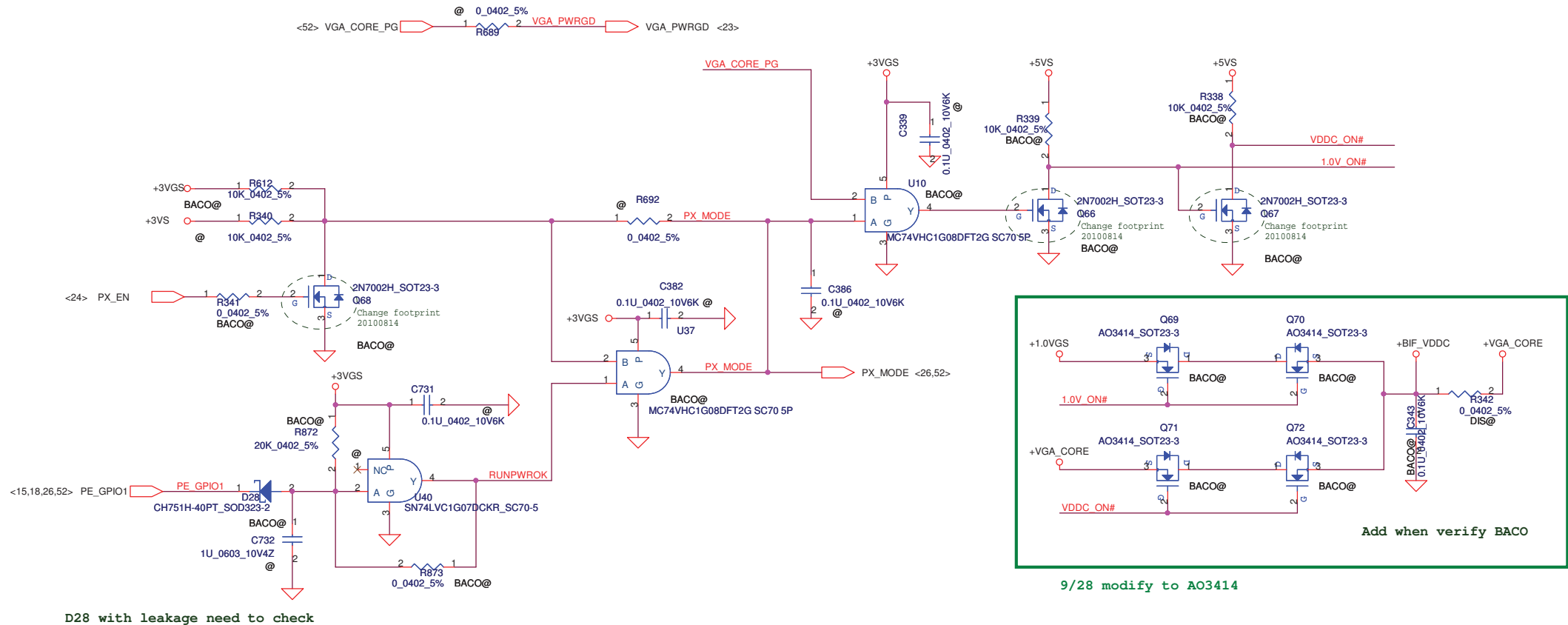


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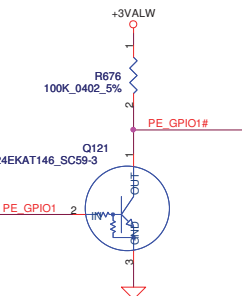




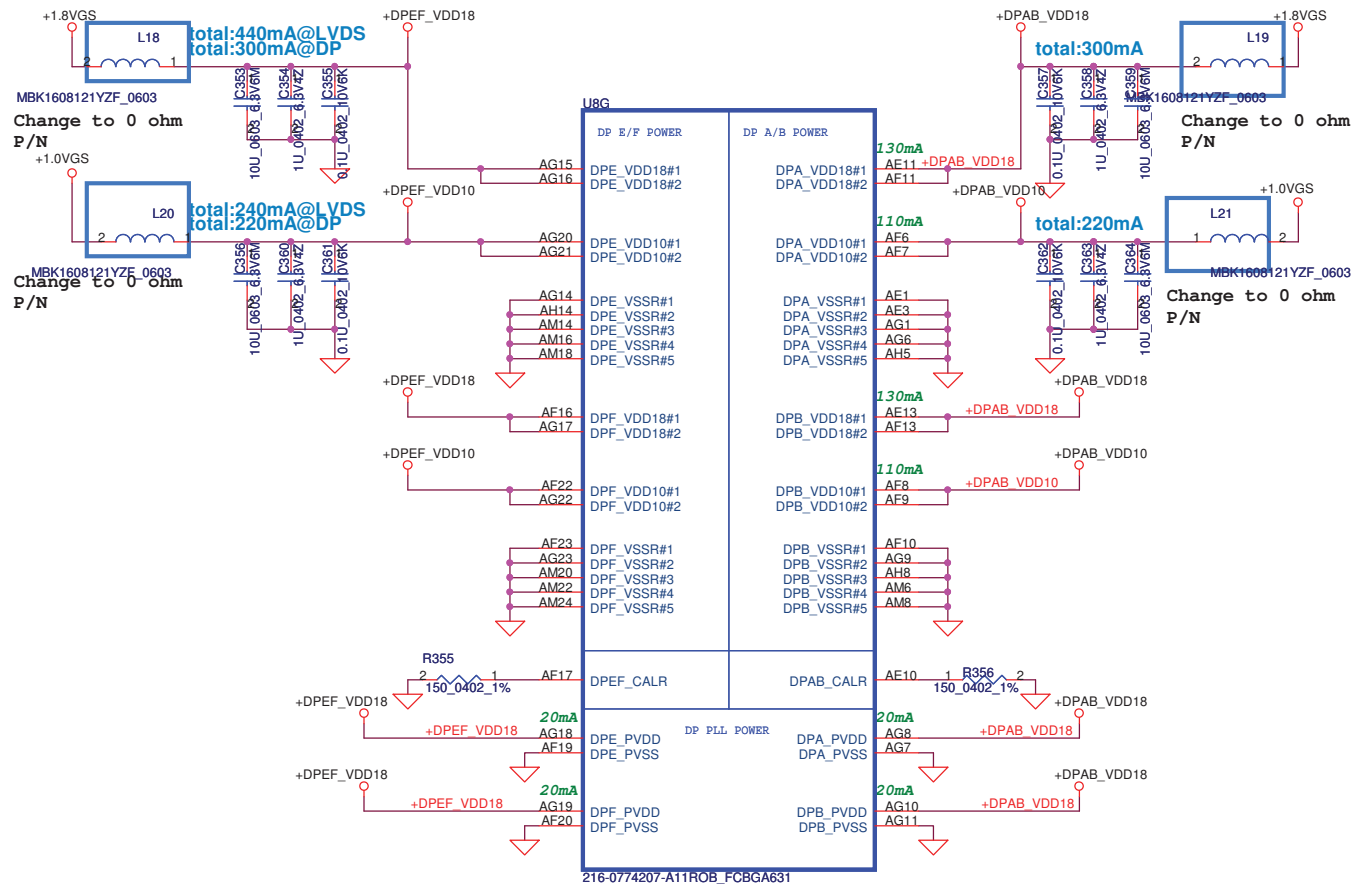


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+VGA_PCIE TO +1.0VGS

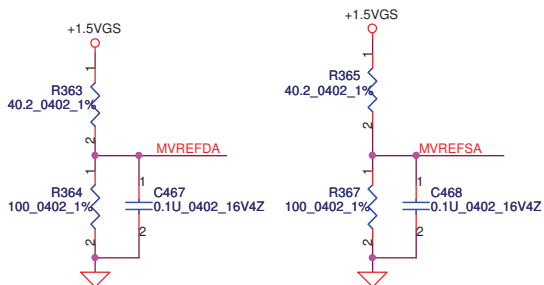


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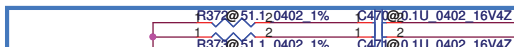
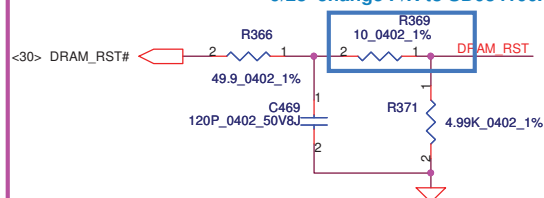
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<30> M_DA[63..0] M_DA[63..0]
<30> M_MA[13..0] M_MA[13..0]
<30> M_DQM[7..0] M_DQM[7..0]
<30> M_DQS[7..0] M_DQS[7..0]
<30> M_DQS#[7..0] M_DQS#[7..0]



PARK SCL has different
recommand

9/28 change P/N to SD034100A80



Route 50ohms single-ended/100ohm diff and keep short
debug only, for clock observation,if not need,
DNI.

M_DA0 K27
M_DA1 J28
M_DA2 H30
M_DA3 H32
M_DA4 G29
M_DA5 F28
M_DA6 F32
M_DA7 F30
M_DA8 C30
M_DA9 F27
M_DA10 A28
M_DA11 C28
M_DA12 E27
M_DA13 G26
M_DA14 D26
M_DA15 F25
M_DA16 A25
M_DA17 C25
M_DA18 E25
M_DA19 D24
M_DA20 F23
M_DA21 F23
M_DA22 D22
M_DA23 F21
M_DA24 E21
M_DA25 D20
M_DA26 F19
M_DA27 D18
M_DA28 F17
M_DA29 C17
M_DA30 A17
M_DA31 C17
M_DA32 E17
M_DA33 D16
M_DA34 F15
M_DA35 A15
M_DA36 D14
M_DA37 F13
M_DA38 A13
M_DA39 C13
M_DA40 E11
M_DA41 A11
M_DA42 C11
M_DA43 F11
M_DA44 A9
M_DA45 C9
M_DA46 F9
M_DA47 D8
M_DA48 E7
M_DA49 A7
M_DA50 C7
M_DA51 F7
M_DA52 A5
M_DA53 E5
M_DA54 C3
M_DA55 F1
M_DA56 G7
M_DA57 G6
M_DA58 G1
M_DA59 G3
M_DA60 J6
M_DA61 J1
M_DA62 J3
M_DA63 J5

U8C

GDDR5 /DDR3

DQA0_0/DQA_0
DQA0_1/DQA_1
DQA0_2/DQA_2
DQA0_3/DQA_3
DQA0_4/DQA_4
DQA0_5/DQA_5
DQA0_6/DQA_6
DQA0_7/DQA_7
DQA0_8/DQA_8
DQA0_9/DQA_9
DQA0_10/DQA_10
DQA0_11/DQA_11
DQA0_12/DQA_12
DQA0_13/DQA_13
DQA0_14/DQA_14
DQA0_15/DQA_15
DQA0_16/DQA_16
DQA0_17/DQA_17
DQA0_18/DQA_18
DQA0_19/DQA_19
DQA0_20/DQA_20
DQA0_21/DQA_21
DQA0_22/DQA_22
DQA0_23/DQA_23
DQA0_24/DQA_24
DQA0_25/DQA_25
DQA0_26/DQA_26
DQA0_27/DQA_27
DQA0_28/DQA_28
DQA0_29/DQA_29
DQA0_30/DQA_30
DQA0_31/DQA_31
DQA1_0/DQA_32
DQA1_1/DQA_33
DQA1_2/DQA_34
DQA1_3/DQA_35
DQA1_4/DQA_36
DQA1_5/DQA_37
DQA1_6/DQA_38
DQA1_7/DQA_39
DQA1_8/DQA_40
DQA1_9/DQA_41
DQA1_10/DQA_42
DQA1_11/DQA_43
DQA1_12/DQA_44
DQA1_13/DQA_45
DQA1_14/DQA_46
DQA1_15/DQA_47
DQA1_16/DQA_48
DQA1_17/DQA_49
DQA1_18/DQA_50
DQA1_19/DQA_51
DQA1_20/DQA_52
DQA1_21/DQA_53
DQA1_22/DQA_54
DQA1_23/DQA_55
DQA1_24/DQA_56
DQA1_25/DQA_57
DQA1_26/DQA_58
DQA1_27/DQA_59
DQA1_28/DQA_60
DQA1_29/DQA_61
DQA1_30/DQA_62
DQA1_31/DQA_63

MEMORY
INTERFACE

GDDR5 /DDR3

MAA0_0/MAA_0
MAA0_1/MAA_1
MAA0_2/MAA_2
MAA0_3/MAA_3
MAA0_4/MAA_4
MAA0_5/MAA_5
MAA0_6/MAA_6
MAA0_7/MAA_7
MAA1_0/MAA_8
MAA1_1/MAA_9
MAA1_2/MAA_10
MAA1_3/MAA_11
MAA1_4/MAA_12
MAA1_5/MAA_13/BA2
MAA1_6/MAA_14/BA0
MAA1_7/MAA_15/BA1
WCKA0_0/DQMA_0
WCKA0B_0/DQMA_1
WCKA0_1/DQMA_2
WCKA0B_1/DQMA_3
WCKA1_0/DQMA_4
WCKA1B_0/DQMA_5
WCKA1_1/DQMA_6
WCKA1B_1/DQMA_7
EDCA0_0/RDQSA_0
EDCA0_1/RDQSA_1
EDCA0_2/RDQSA_2
EDCA0_3/RDQSA_3
EDCA1_0/RDQSA_4
EDCA1_1/RDQSA_5
EDCA1_2/RDQSA_6
EDCA1_3/RDQSA_7
DDBIA0_0/WDQSA_0
DDBIA0_1/WDQSA_1
DDBIA0_2/WDQSA_2
DDBIA0_3/WDQSA_3
DDBIA1_0/WDQSA_4
DDBIA1_1/WDQSA_5
DDBIA1_2/WDQSA_6
DDBIA1_3/WDQSA_7
ADBBIA0/ODTA0
ADBBIA1/ODTA1
CLKA0
CLKA0B
CLKA1
CLKA1B
RASA0B
RASA1B
CASA0B
CASA1B
CSA0B_0
CSA0B_1
CSA1B_0
CSA1B_1
CKEA0
CKEA1
WEA0B
WEA1B

GDDR5

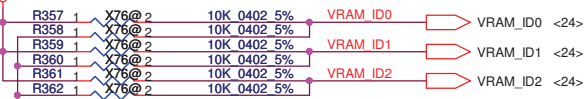
MAA1_8
MAA0_8

M MA13

CLKTESTA
CLKTESTB

216-0774207-A11ROB_FCBGA631

+1.8VGS

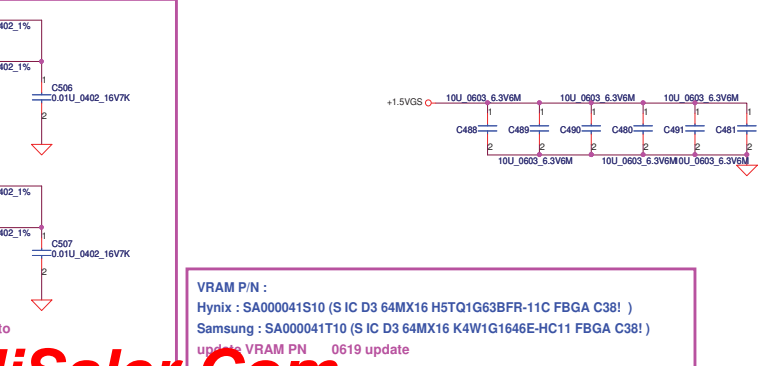
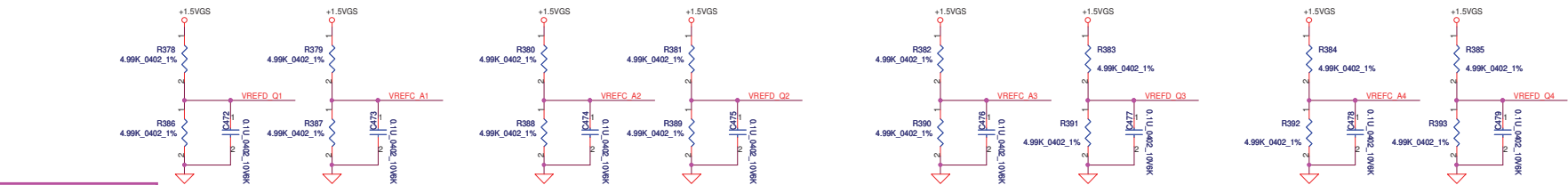
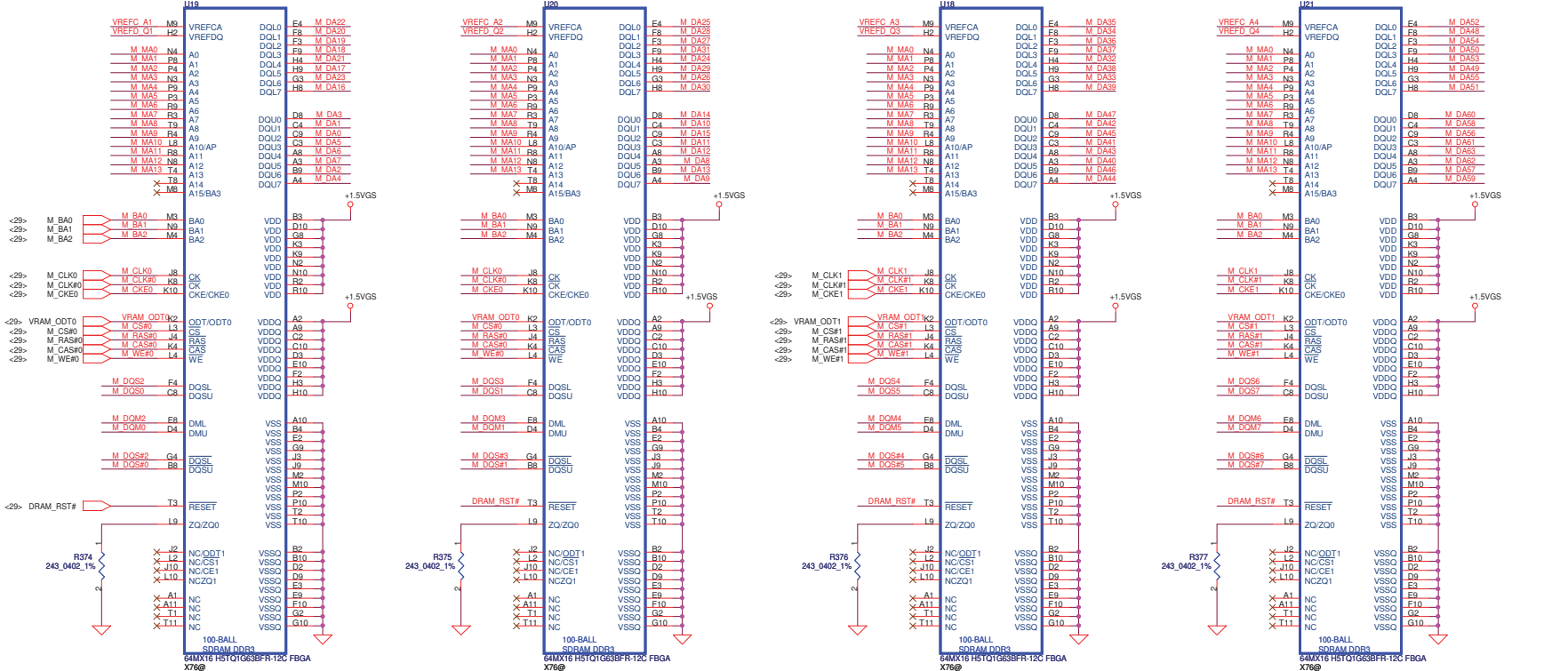
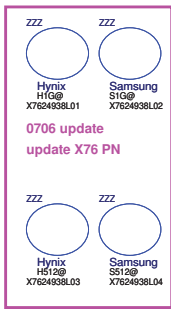


Vendor	VRAM_ID0	VRAM_ID1	VRAM_ID2
Hynix 512MB PN:SA000032460	R357	R360	R362
Samsung 512MB PN:SA000035700	R358	R359	R362
Hynix 1GB PN:SA00003VS20	R357	R360	R361
Samsung 1GB PN:SA00003MQ20	R358	R359	R361

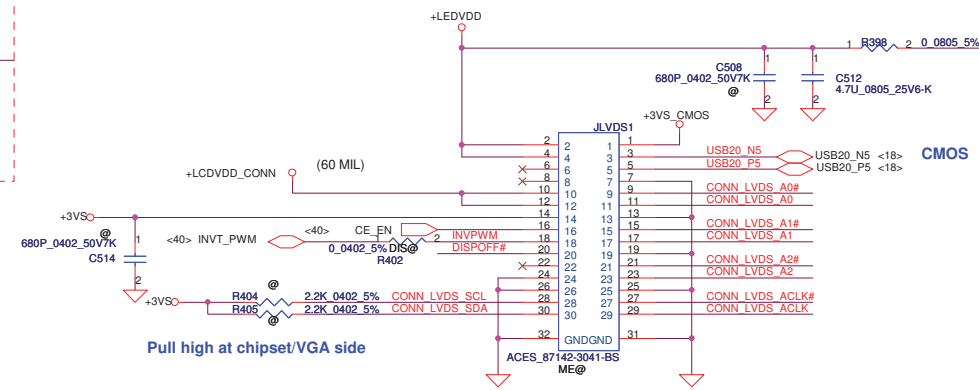
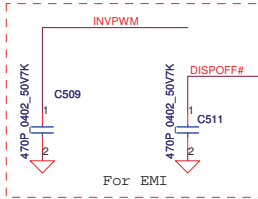
Security Classification		Compal Secret Data	
Issued Date	2010/07/12	Deciphered Date	2012/07/11
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Compal Electronics, Inc.			
RobsonXT-S3 MEM Interface			
Size B	Document Number		Rev 0.2
Date:	Friday, November 26, 2010		Sheet 29 of 59

<29> M_DA[63..0] M_DA[63..0]
 <29> M_MA[13..0] M_MA[13..0]
 <29> M_DQM[7..0] M_DQM[7..0]
 <29> M_DQS[7..0] M_DQS[7..0]
 <29> M_DQS[7..0] M_DQS[7..0]

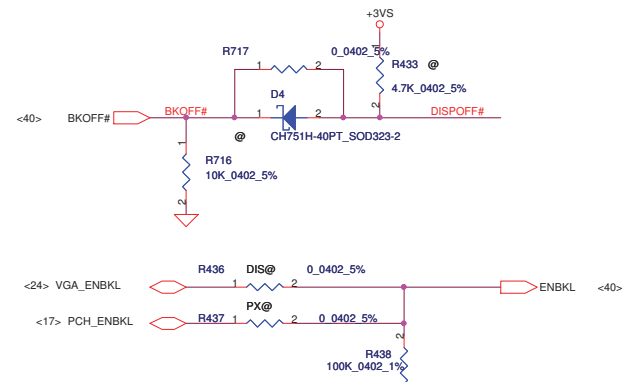
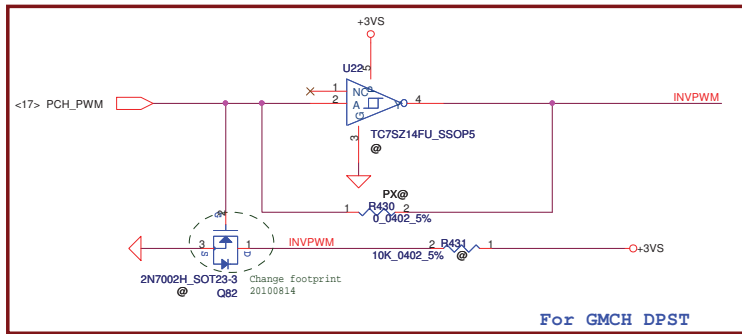


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Issued Date	2010/07/12	Deciphered Date	2012/07/11	Title
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Size	C	Document Number		Rev 0.2
Date:	Friday, November 26, 2010	Sheet	30	of 59



Pull high at chipset/VGA side

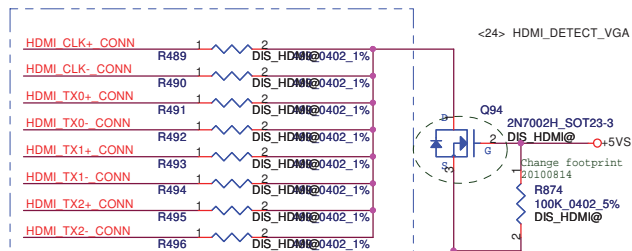
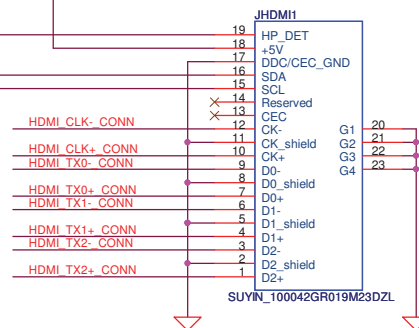
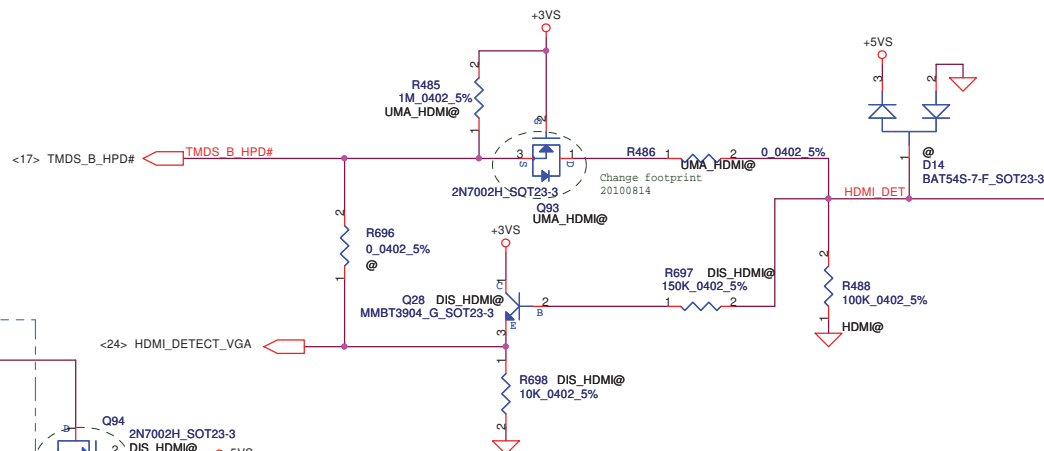
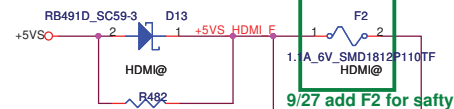
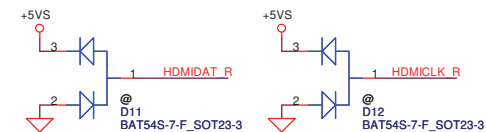
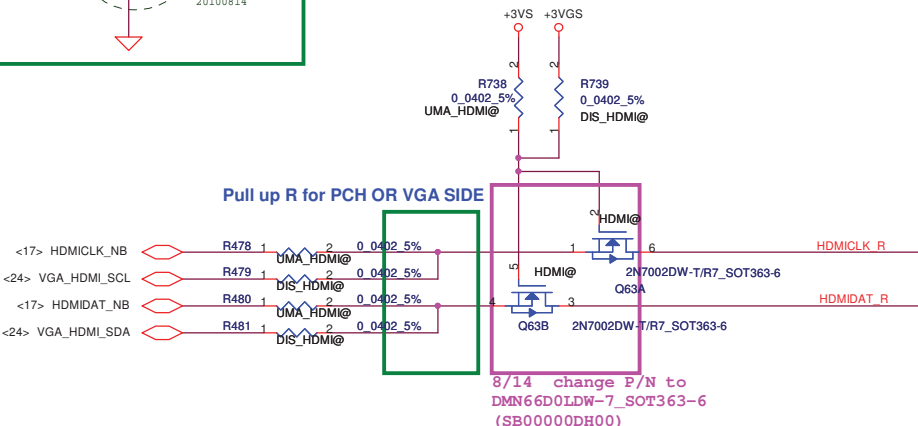
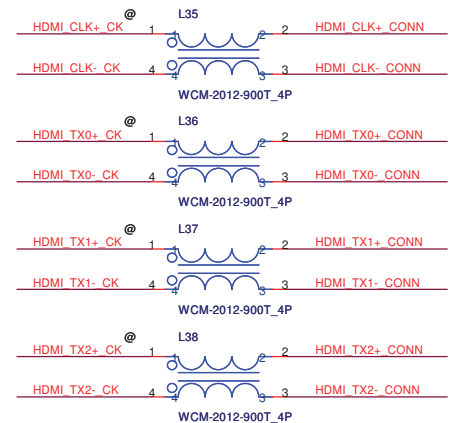
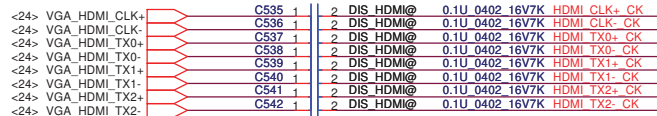
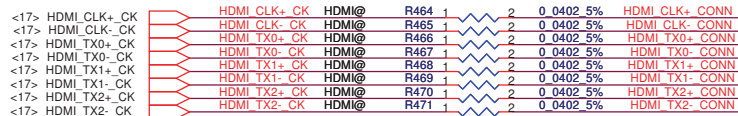
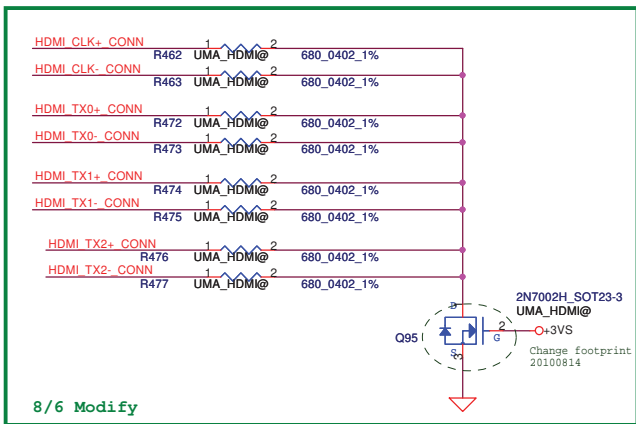
- | | | | | | | | | |
|---------------------|----------------|--------|----|---|------|---|------|-----------------|
| <24> VGA_LVDS_SCL | VGA LVDS_SCL | 0.0402 | 5% | 2 | DIS0 | 1 | R409 | CONN LVDS_SCL |
| <24> VGA_LVDS_SDA | VGA LVDS_SDA | 0.0402 | 5% | 2 | DIS0 | 1 | R410 | CONN LVDS_SDA |
| <23> VGA_LVDS_A0 | VGA LVDS_A0 | 0.0402 | 5% | 2 | DIS0 | 1 | R411 | CONN LVDS_A0 |
| <23> VGA_LVDS_A0# | VGA LVDS_A0# | 0.0402 | 5% | 2 | DIS0 | 1 | R412 | CONN LVDS_A0# |
| <23> VGA_LVDS_A1 | VGA LVDS_A1 | 0.0402 | 5% | 2 | DIS0 | 1 | R413 | CONN LVDS_A1 |
| <23> VGA_LVDS_A1# | VGA LVDS_A1# | 0.0402 | 5% | 2 | DIS0 | 1 | R414 | CONN LVDS_A1# |
| <23> VGA_LVDS_A2 | VGA LVDS_A2 | 0.0402 | 5% | 2 | DIS0 | 1 | R415 | CONN LVDS_A2 |
| <23> VGA_LVDS_A2# | VGA LVDS_A2# | 0.0402 | 5% | 2 | DIS0 | 1 | R416 | CONN LVDS_A2# |
| <23> VGA_LVDS_ACLK | VGA LVDS_ACLK | 0.0402 | 5% | 2 | DIS0 | 1 | R417 | CONN LVDS_ACLK |
| <23> VGA_LVDS_ACLK# | VGA LVDS_ACLK# | 0.0402 | 5% | 2 | DIS0 | 1 | R418 | CONN LVDS_ACLK# |
| <17> EDID_CLK | EDID_CLK | 0.0402 | 5% | 2 | PX0 | 1 | R419 | CONN LVDS_SCL |
| <17> EDID_DATA | EDID_DATA | 0.0402 | 5% | 2 | PX0 | 1 | R420 | CONN LVDS_SDA |
| <17> LVDS_A0 | LVDS_A0 | 0.0402 | 5% | 2 | PX0 | 1 | R421 | CONN LVDS_A0 |
| <17> LVDS_A0# | LVDS_A0# | 0.0402 | 5% | 2 | PX0 | 1 | R422 | CONN LVDS_A0# |
| <17> LVDS_A1 | LVDS_A1 | 0.0402 | 5% | 2 | PX0 | 1 | R423 | CONN LVDS_A1 |
| <17> LVDS_A1# | LVDS_A1# | 0.0402 | 5% | 2 | PX0 | 1 | R424 | CONN LVDS_A1# |
| <17> LVDS_A2 | LVDS_A2 | 0.0402 | 5% | 2 | PX0 | 1 | R425 | CONN LVDS_A2 |
| <17> LVDS_A2# | LVDS_A2# | 0.0402 | 5% | 2 | PX0 | 1 | R427 | CONN LVDS_A2# |
| <17> LVDS_ACLK | LVDS_ACLK | 0.0402 | 5% | 2 | PX0 | 1 | R428 | CONN LVDS_ACLK |
| <17> LVDS_ACLK# | LVDS_ACLK# | 0.0402 | 5% | 2 | PX0 | 1 | R429 | CONN LVDS_ACLK# |

[illegible]

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				B	LA-6751P	0.2
				Date:	Friday, November 26, 2010	Sheet 31 of 59

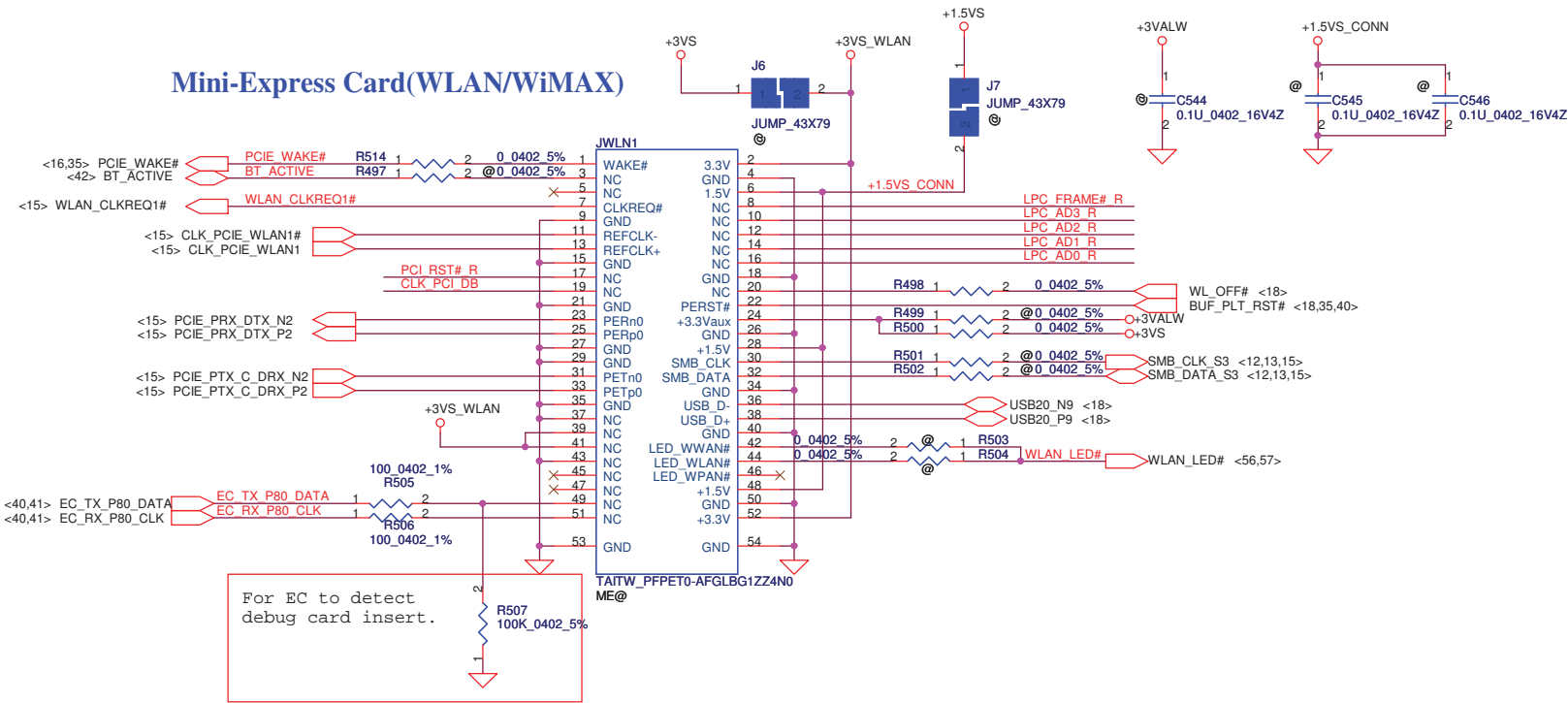
WWW.AliSaler.Com





Security Classification		Compal Secret Data		Compal Electronics, Ltd.	
Issued Date	2010/07/12	Deciphered Date	2012/07/11	Title	HDMI CONN
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				Custom	LA-6751P
				Date	Friday, November 26, 2010
				Sheet	33 of 59
				Rev	0.2

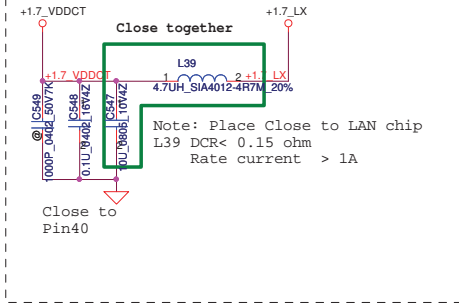
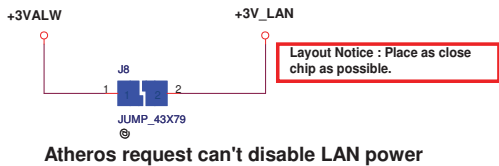
Mini-Express Card for WLAN/WiMAX(Half)



Reserve for SW mini-pcie debug card.
Series resistors closed to KBC side.

Component	Value	Pin	Signal
LPC_FRAME# R	R508	1	LPC_FRAME# <14,40>
LPC_AD3 R	R509	1	LPC_AD3 <14,40>
LPC_AD2 R	R510	1	LPC_AD2 <14,40>
LPC_AD1 R	R511	1	LPC_AD1 <14,40>
LPC_AD0 R	R512	1	LPC_AD0 <14,40>
PCI_RST# R	R513	1	CLK_PCIE_DB <15>
CLK_PCIE_DB	R513	1	CLK_PCIE_DB <15>

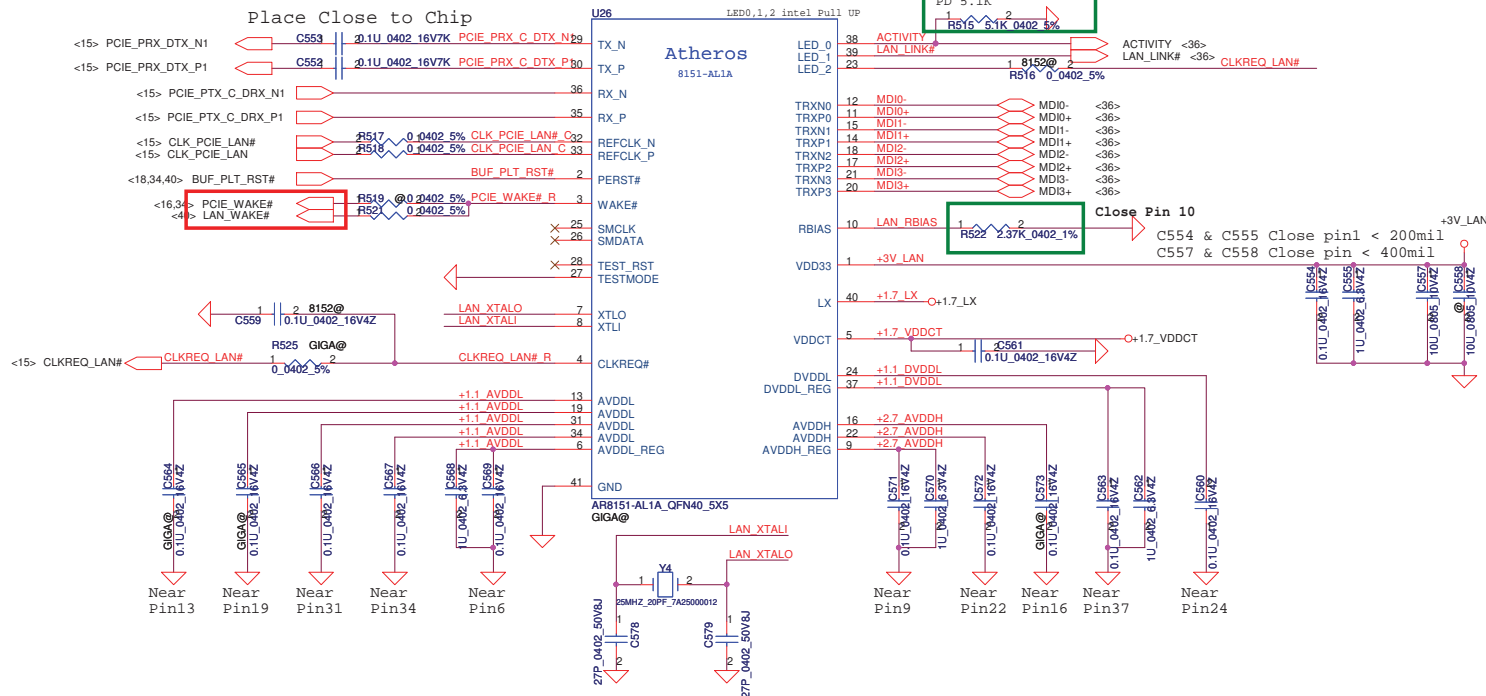
Security Classification	Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2010/07/12	Deciphered Date	2012/07/11	Title
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				Size Document Number
				LA-6751P
				Rev 0.2
				Date: Friday, November 26, 2010
				Sheet 34 of 59



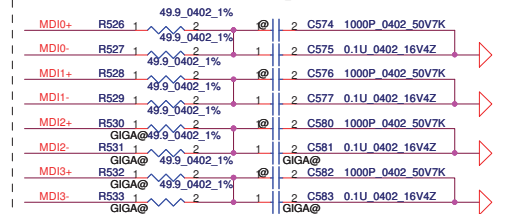
Power On strapping

Pin	Description	Chip Default
LED0	H:Over Clock Enable L:Over Clock Disable *	H
LED2	H:SWR Switch mode regulator Select * AR8151 Pin23=LED2. AR8152, Pin23 is CLKREQ	--

U26 8152@
S IC AR8152-AL1E QFN 40P E-LAN CTRL



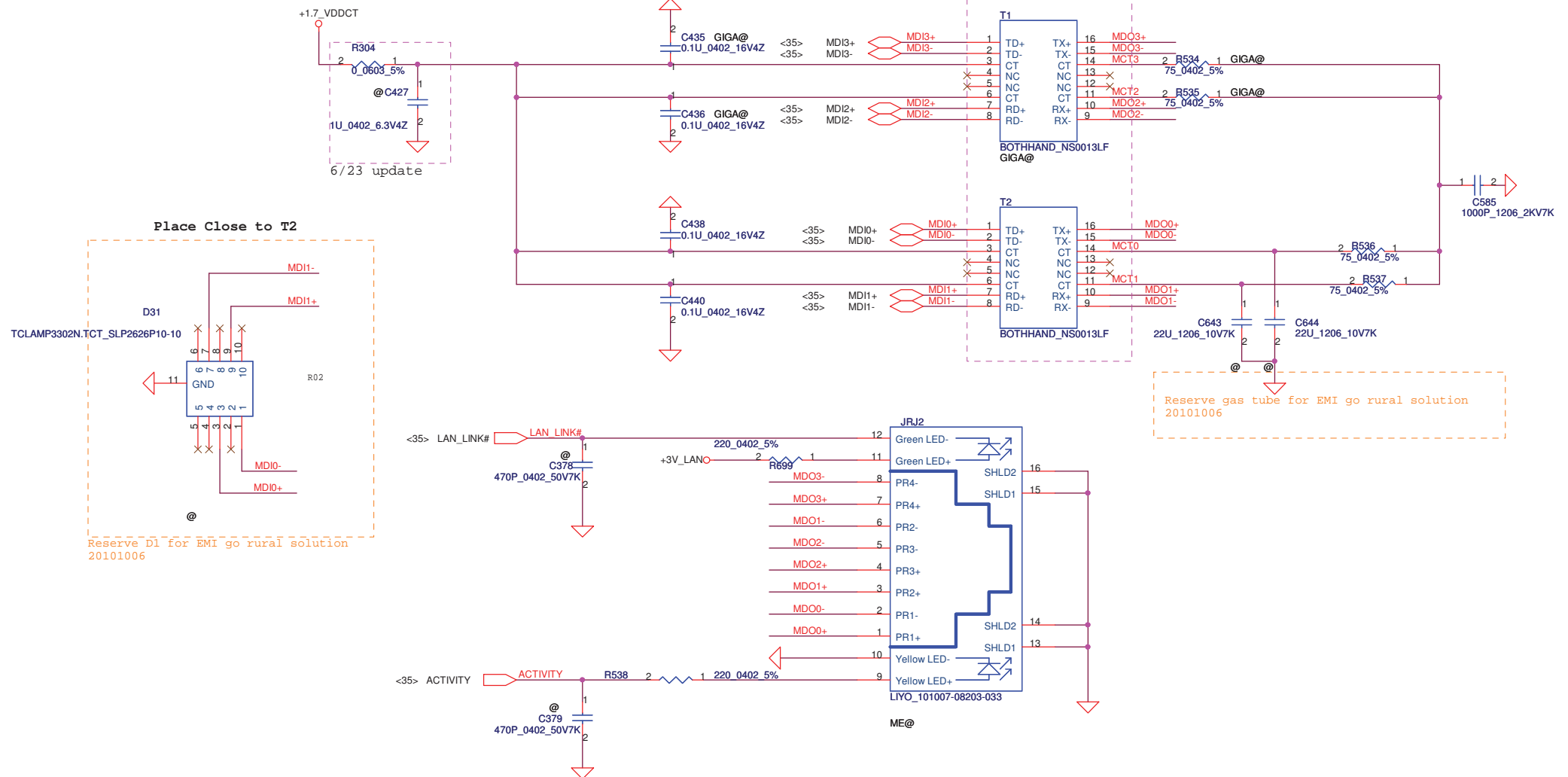
Place Close to LAN chip



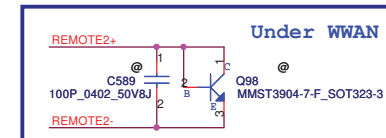
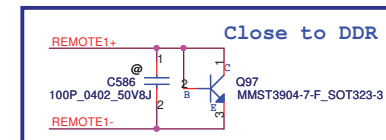
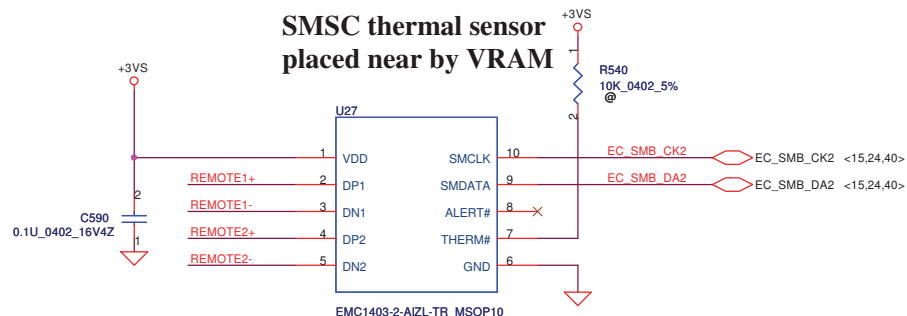
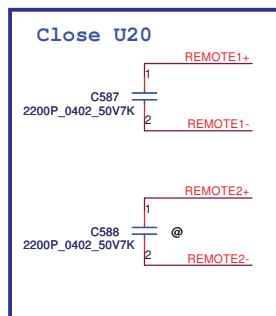
	Pin4	Configure		Pin23	Configure
AR8152	VDDCT_REG	R525	C559 *	CLKREQn	*
AR8151	CLKREQn	*		LED[2]	

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Issued Date	2010/07/12	Deciphered Date	2012/07/11	Title	LAN-AR8151/8152
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				Document Number	LA-6751P
				Date: Friday, November 26, 2010	Sheet 35 of 59

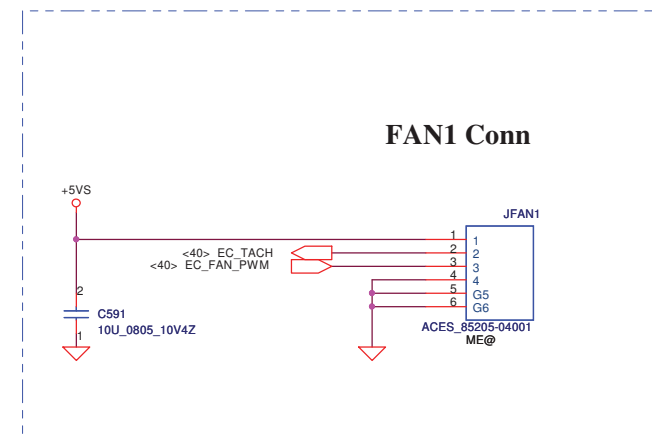
8/23 Change T1,T2 P/N to SP050006E00



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Issued Date	2010/07/12	Deciphered Date	2012/07/11	Title	
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				Size	Document Number
				LA-6751P	
				Date:	Friday, November 26, 2010
				Sheet	36 of 59
				Rev	0.2

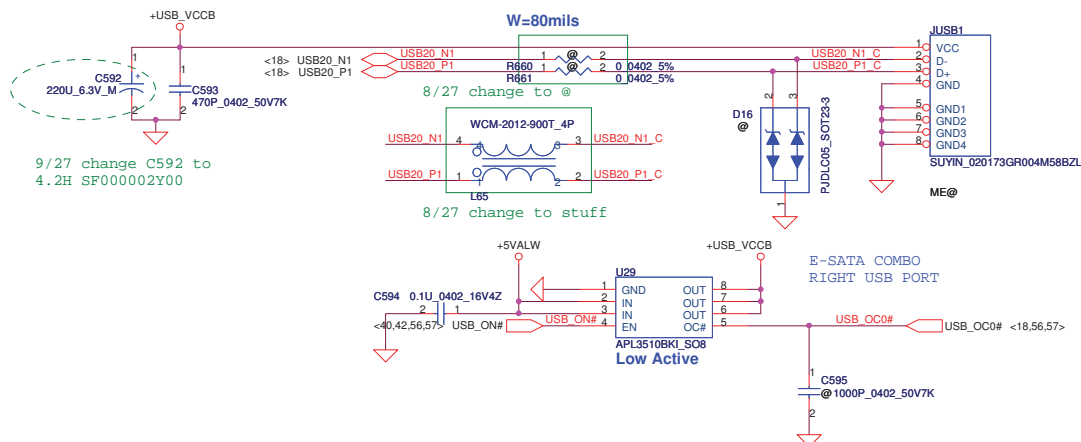


REMOTE1,2+/-:
Trace width/space:10/10 mil
Trace length:<8"

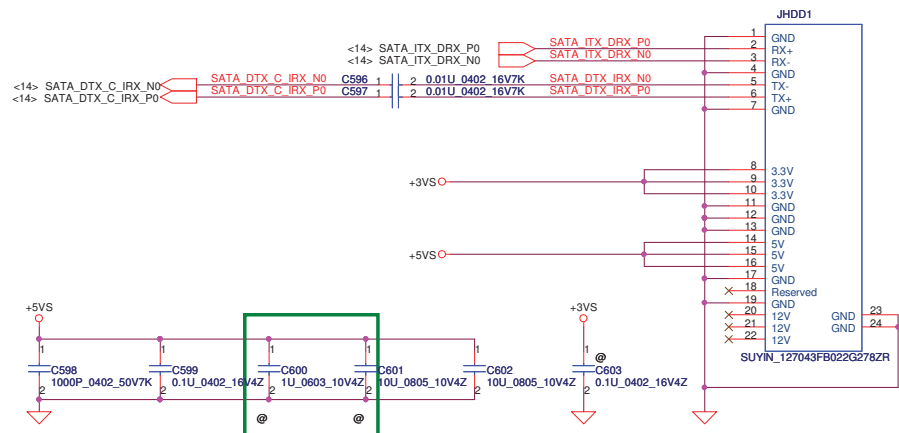


Security Classification		Compal Secret Data		Compal Electronics, Ltd.	
Issued Date	2010/07/12	Deciphered Date	2012/07/11	Title	EMC1403 Thermal sensor/FAN
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				Date: Friday, November 26, 2010	Sheet 37 of 59

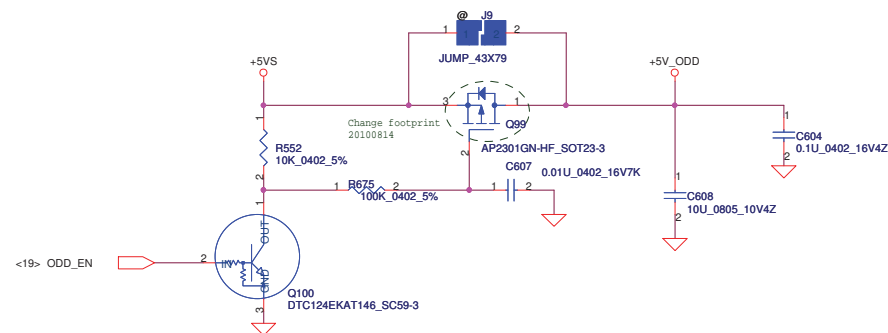
Left USB Conn.



SATA HDD Conn.



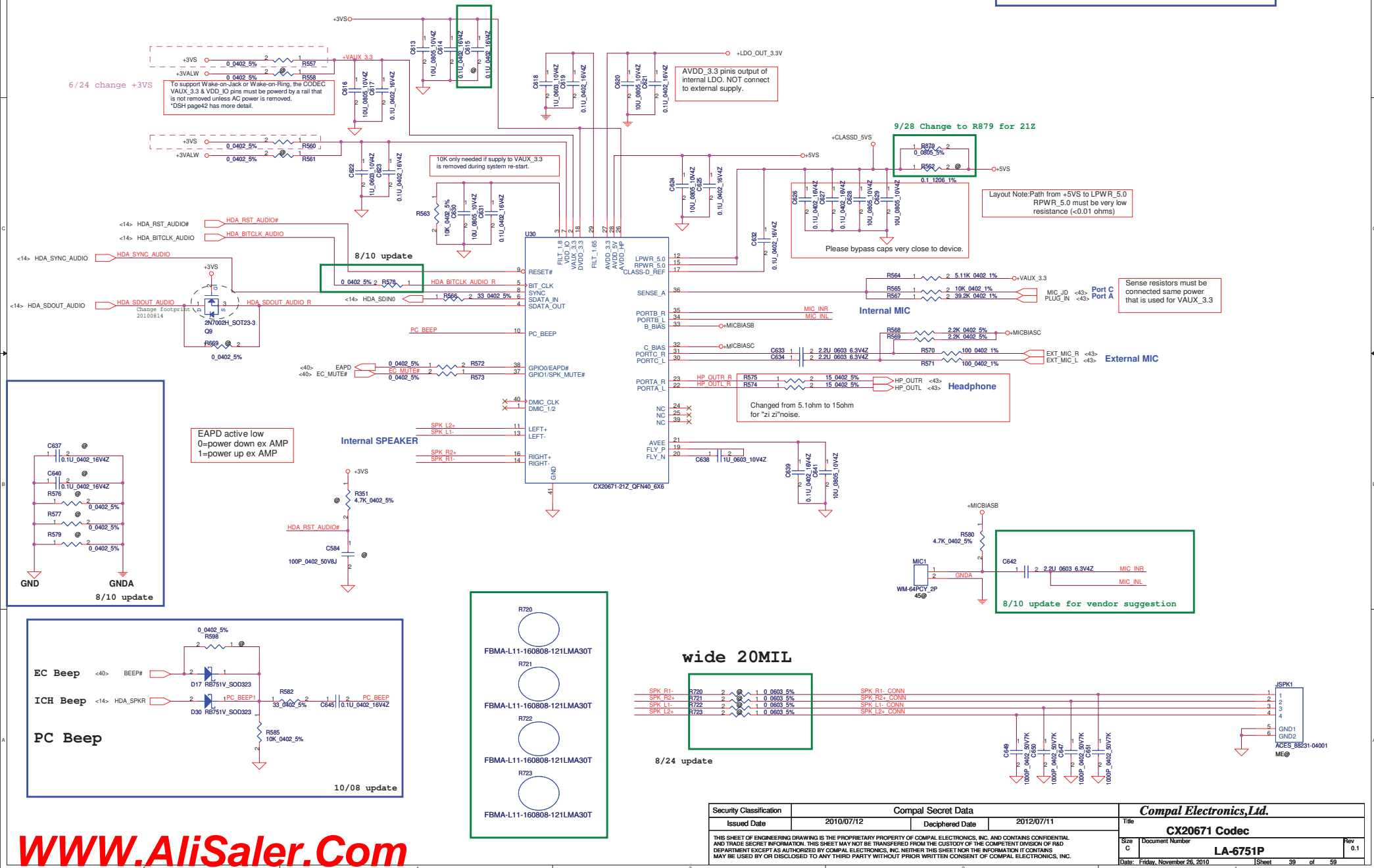
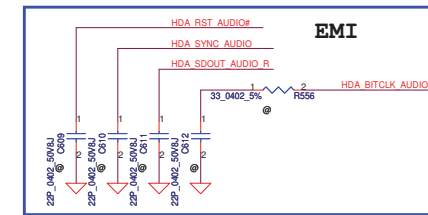
ODD Power Control



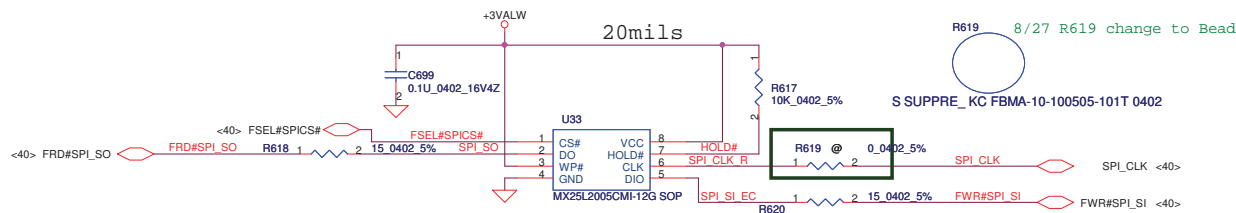
Security Classification		Compal Secret Data		Compal Electronics, Inc.						
Issued Date		2010/07/12	Deciphered Date	2012/07/11	Title	HDD/ODD Connector				
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					B	LA-6751P	0.2			
					Date:	Friday, November 26, 2010	Sheet	38	of	59

CX20671
High Definition Audio Codec SoC
With Integrated Class-D Stereo
Amplifier.
An integrated 5 V to 3.3 V Low-dropout
voltage regulator (LDO).
An integrated 3.3 V to 1.8V Low-dropout
voltage regulator (LDO).

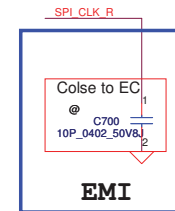
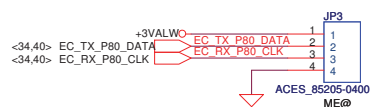
9/27 Update U30 P/N to SA00003K410



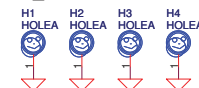
FOR EC 128KB SPI ROM
(150mil PACKAGE)
SA00003FL10
SA00003JD00



EC DEBUG PORT



H_3P8



H_3P3



H_3P0x4P5N



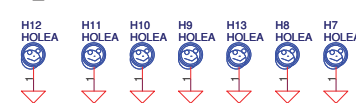
H_3P0N



H_6P0



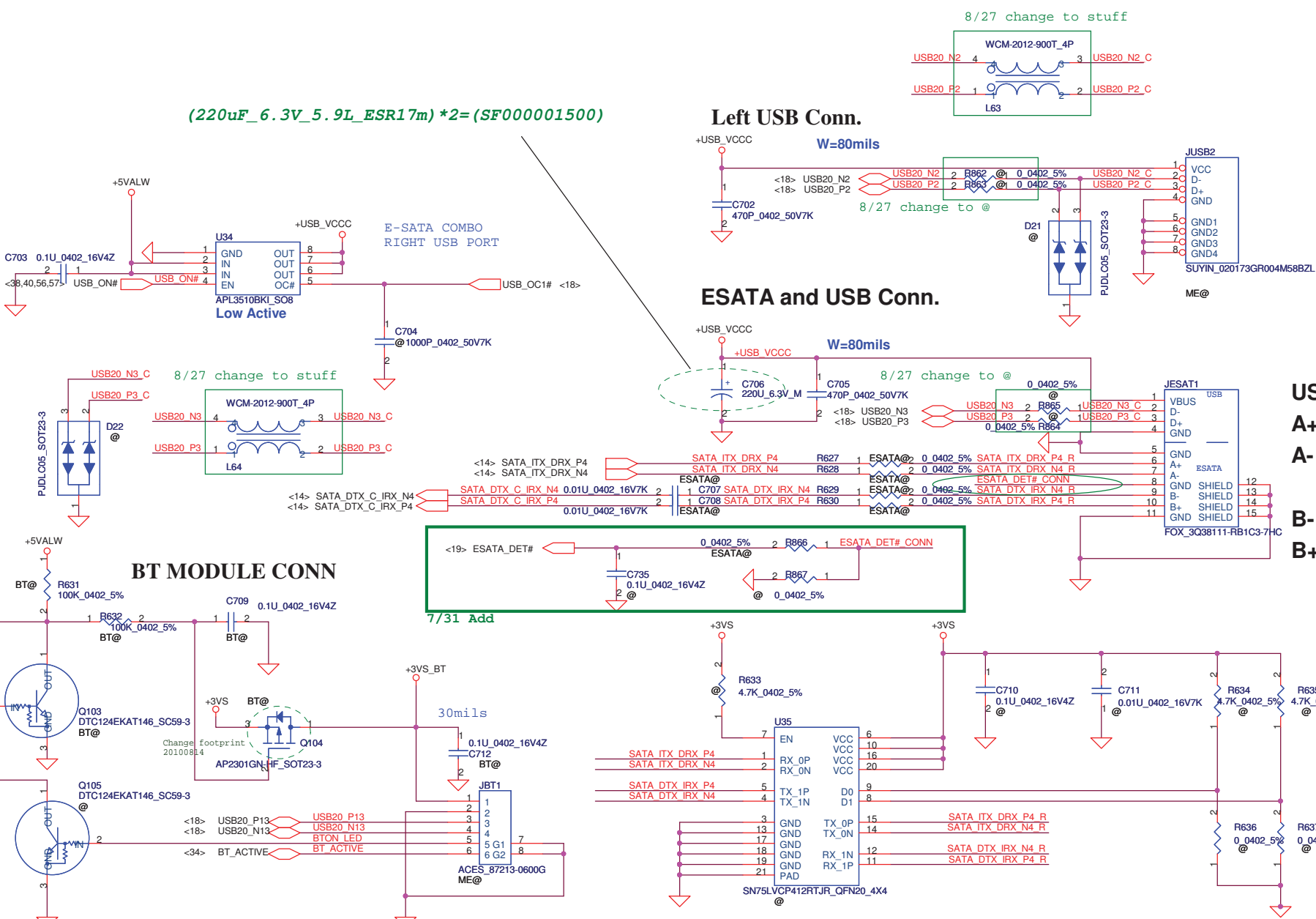
H_2P8



H_5P5N



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(220uF_6.3V_5.9L_ESR17m)*2=(SF000001500)

Left USB Conn.

W=80mils

ESATA and USB Conn.

W=80mils

BT MODULE CONN

USB
A+ = RXP
A- = RXN
B- = TXN
B+ = TXP

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				Size	Document Number		Rev	
				Custom	LA-6751P		0.2	
				Date: Friday, November 26, 2010				
				Sheet 42 of 59				

Bottom Side

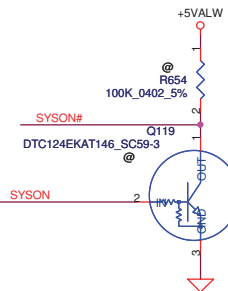
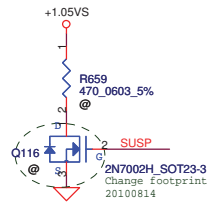
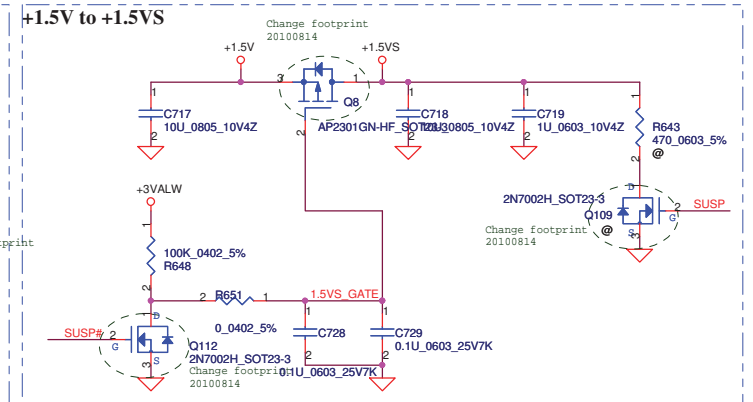
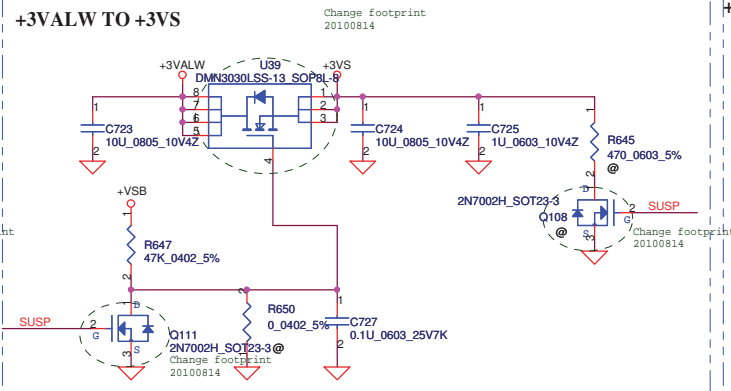
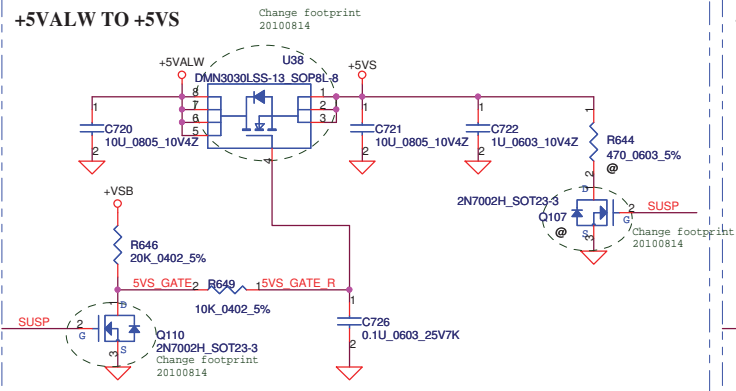


EMI REQUEST 1ST = SCA00000E00
2ST = SCA00000R00



8/5 modify

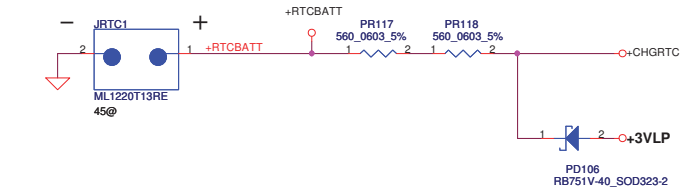
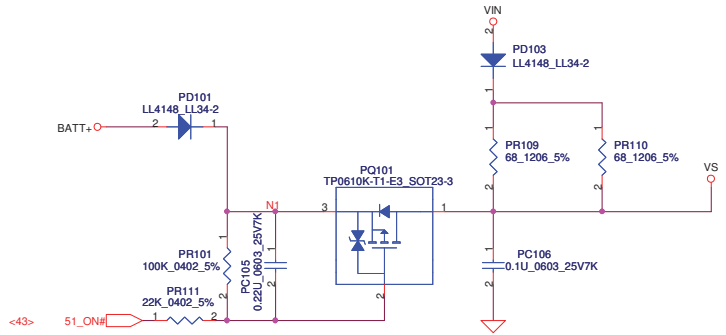
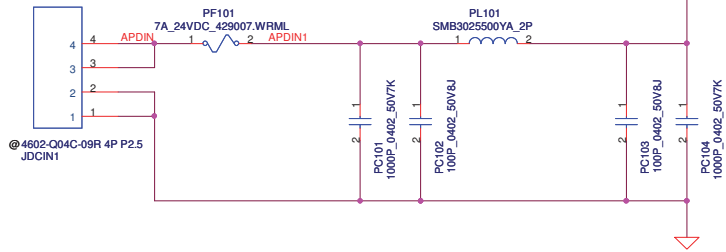
Sheet 43 of 59



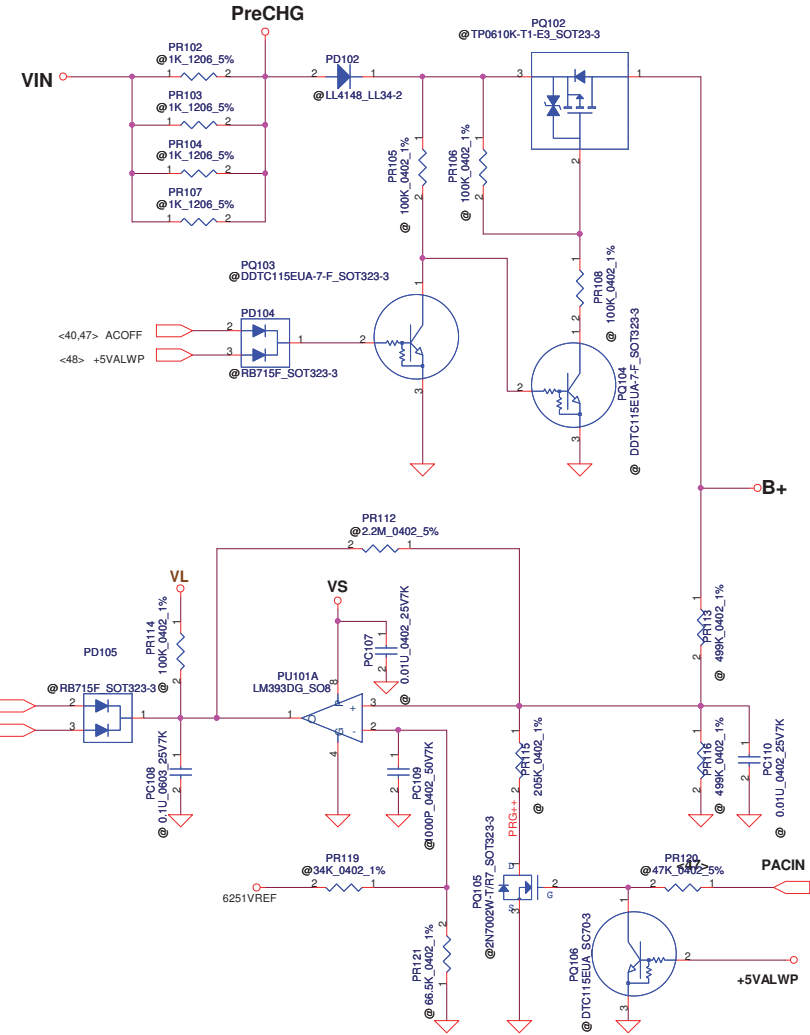
Security Classification				Compal Secret Data				Compal Electronics, Inc.				
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								Size	Document Number			Rev
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DC030006J00

VIN



Precharge detector
15.97V/14.84V FOR
ADAPTOR



ACIN

Precharge detector

	Min.	typ.	Max.
L-->H	14.991V	15.381V	15.782V
H-->L	13.860V	14.247V	14.621V

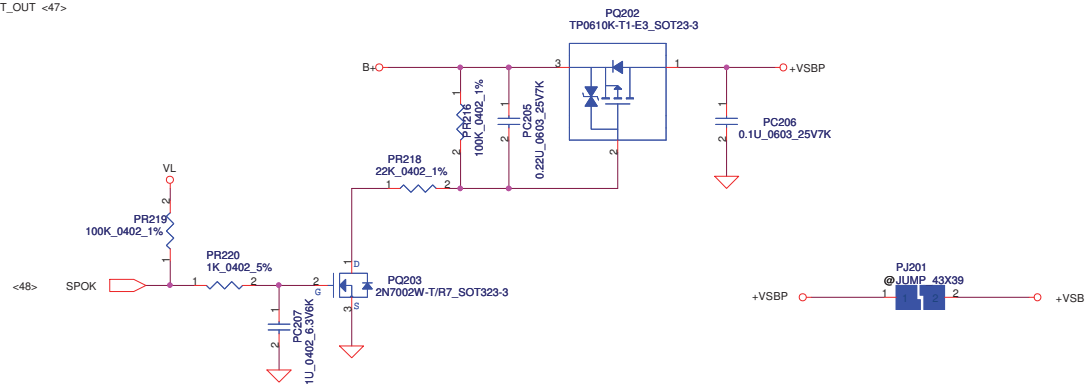
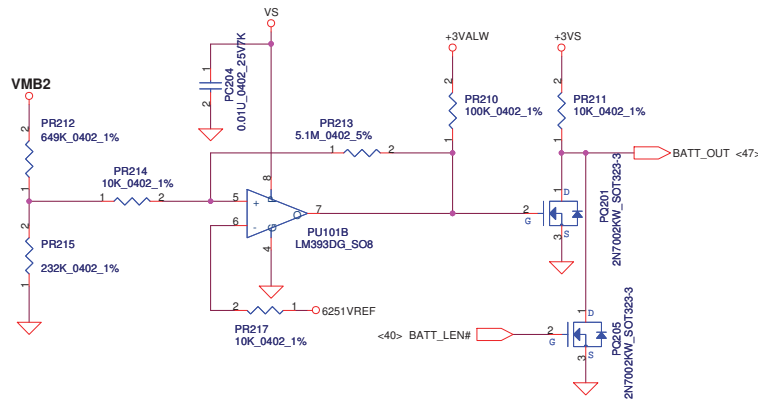
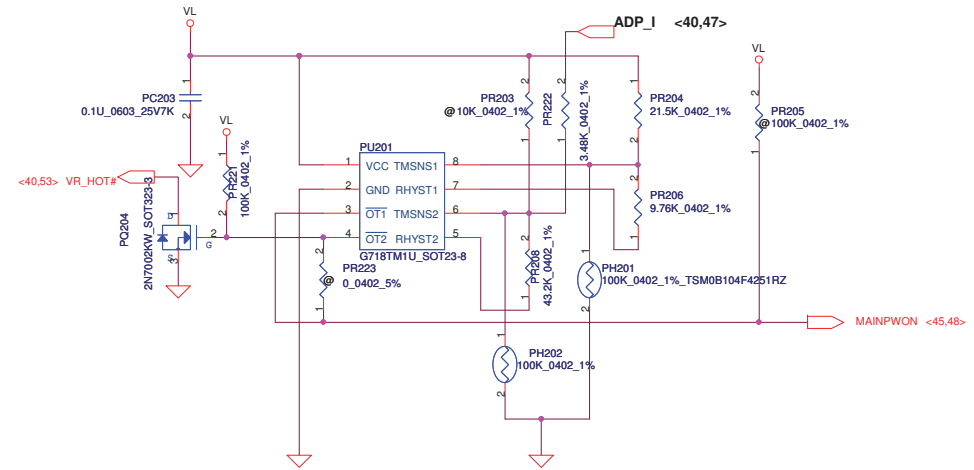
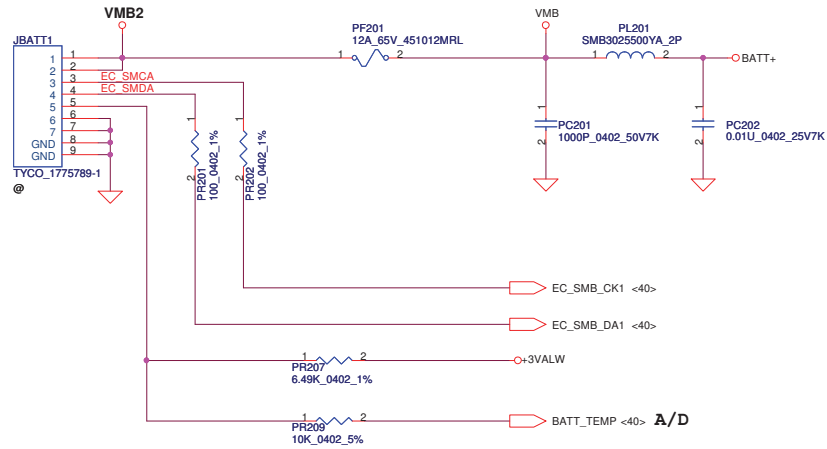
BATT ONLY

Precharge detector

	Min.	typ.	Max.
L-->H	7.196V	7.349V	7.505V
H-->L	6.138V	6.214V	6.056V

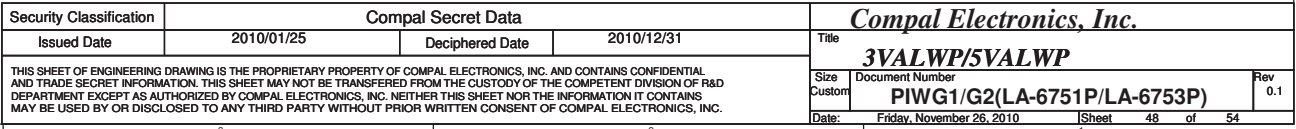
Security Classification	Compal Secret Data			Compal Electronics, Inc.		
Issued Date	2010/01/25	Deciphered Date	2010/12/31	Title PWR DCIN / Vin Detector / Pre-charge		
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				Custom	PIWG1/G2(LA-6751P/LA-6753P)	0.1
				Date:	Friday, November 26, 2010	Sheet 45 of 54

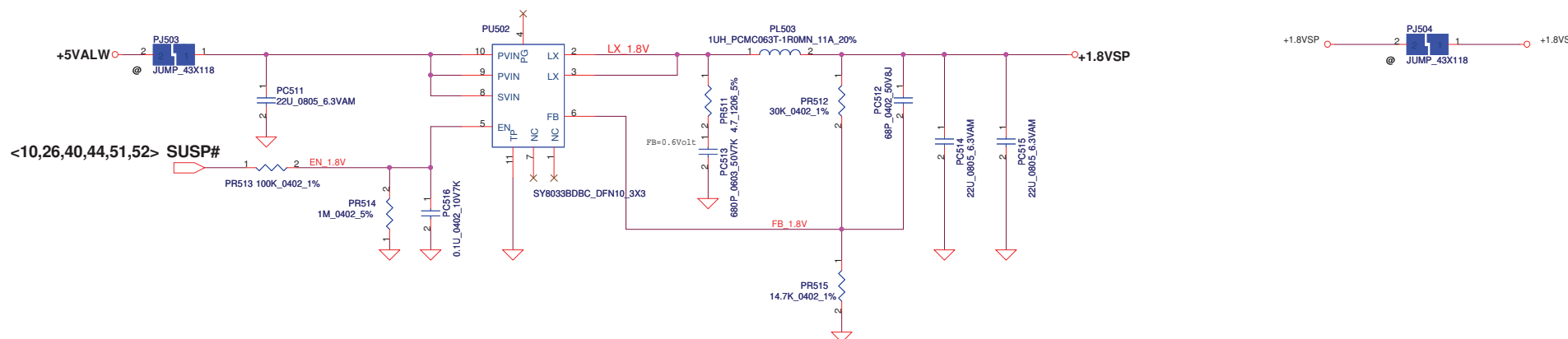
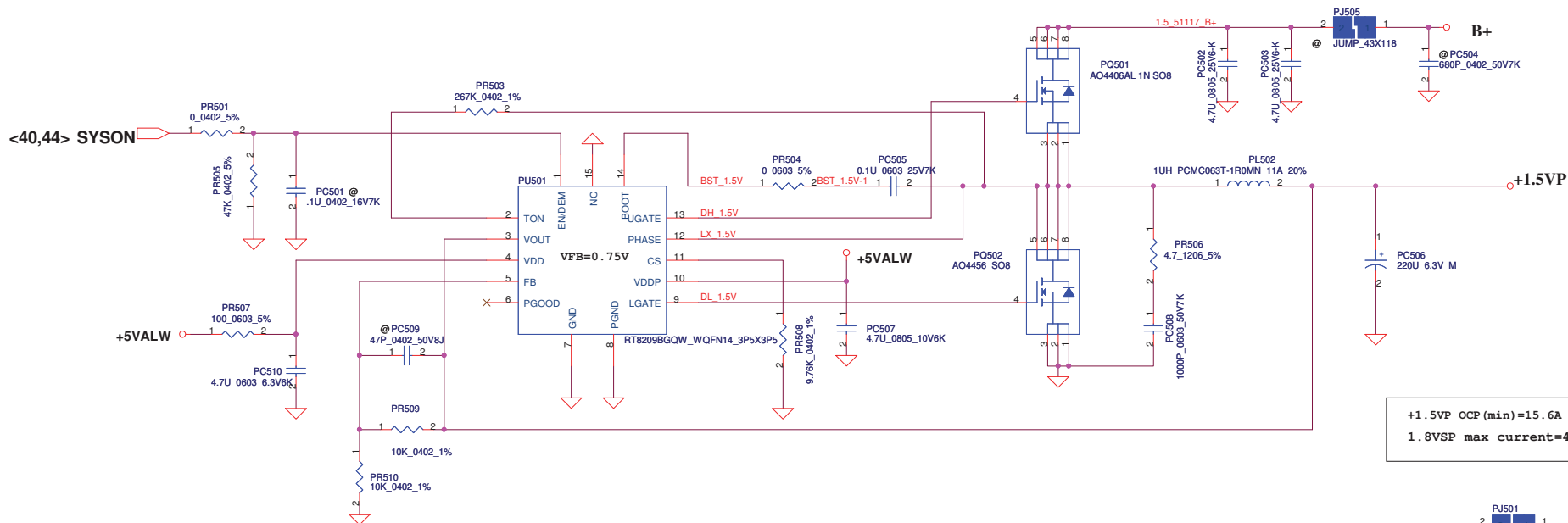
PH201 under CPU bottom side :
CPU thermal protection at 92 degree C
Recovery at 56 degree C



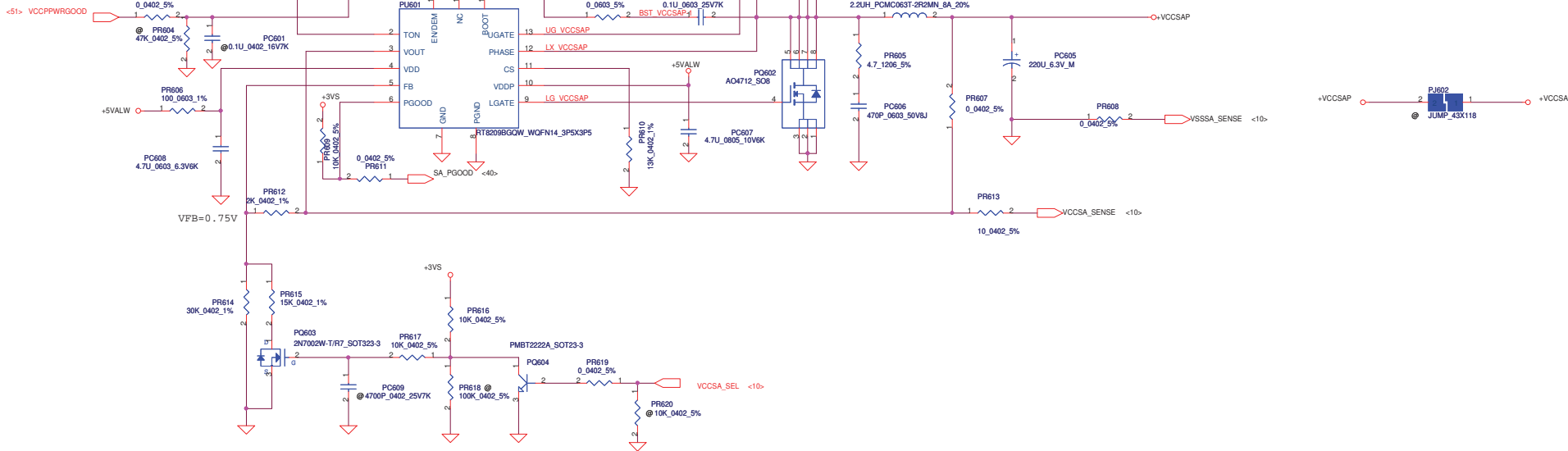
Security Classification		Compal Secret Data		Compal Electronics, Inc.		
Issued Date	2010/01/25	Deciphered Date	2010/12/31	Title	PWR-BATTERY CONN/OTP	
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Size	Document Number	Rev			0.1
Custom	PIWG1/G2(LA-6751P/LA-6753P)				
Date:	Friday, November 26, 2010	Sheet	49	of	54

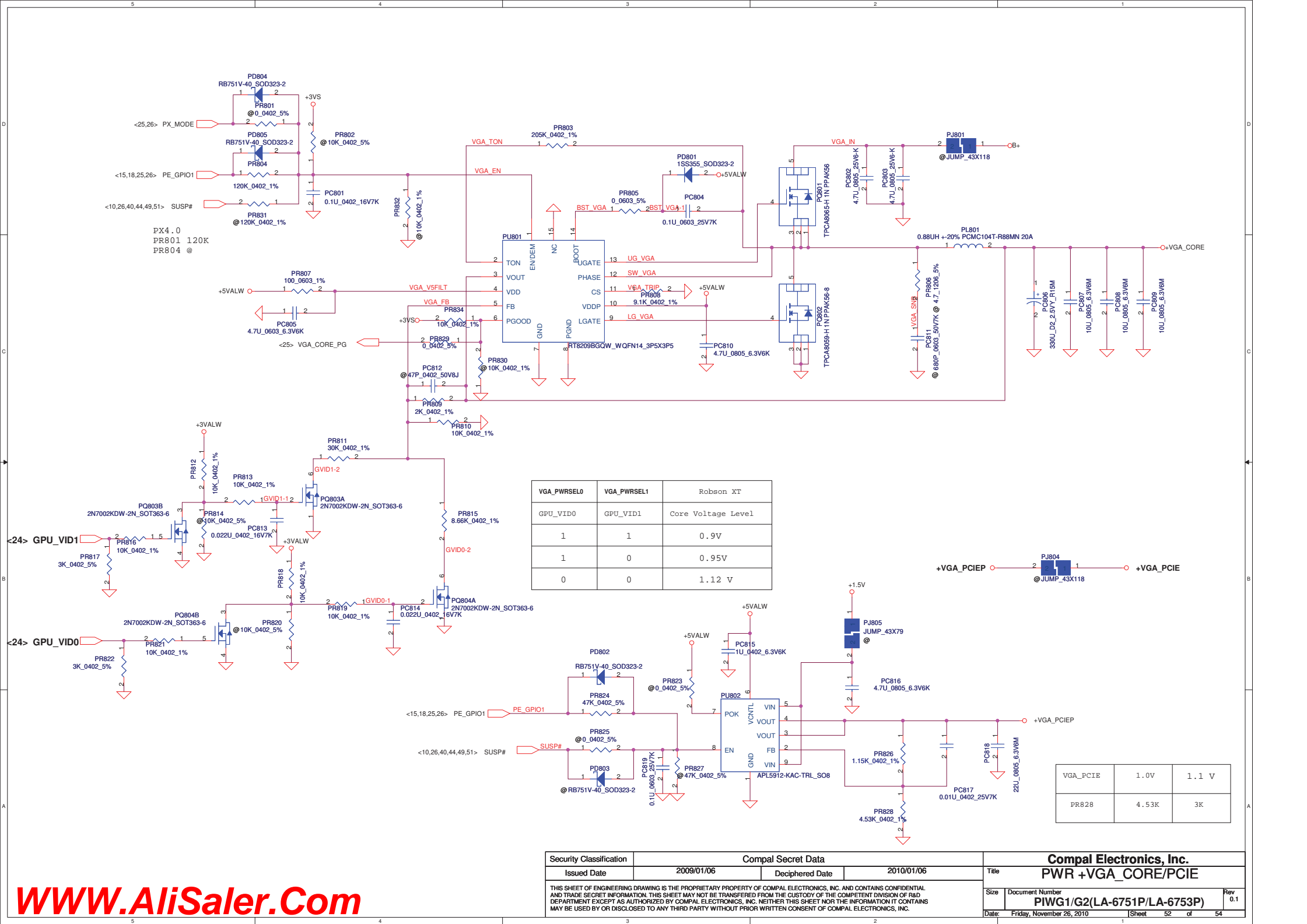


+VCCSAP OCP (min)=6.28A

+VCCSAP
@ JUMP_43X118
+VCCSA



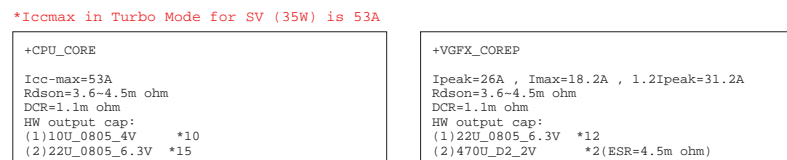
Size Custom	Document Number PIWG1/G2(LA-6751P/LA-6753P)	Rev 0.1
Date:	Friday, November 26, 2010	Sheet 51 of 54



VGA_PWRSEL0	VGA_PWRSEL1	Robson XT
GPU_VID0	GPU_VID1	Core Voltage Level
1	1	0.9V
1	0	0.95V
0	0	1.12 V

VGA_PCIE	1.0V	1.1 V
PR828	4.53K	3K

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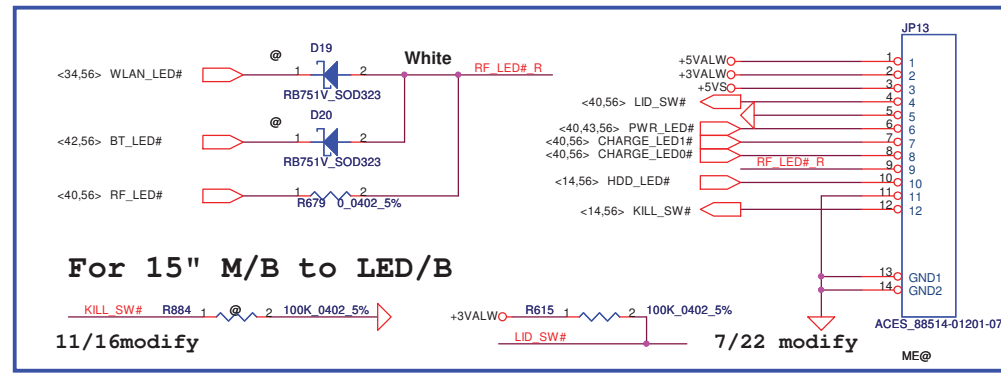
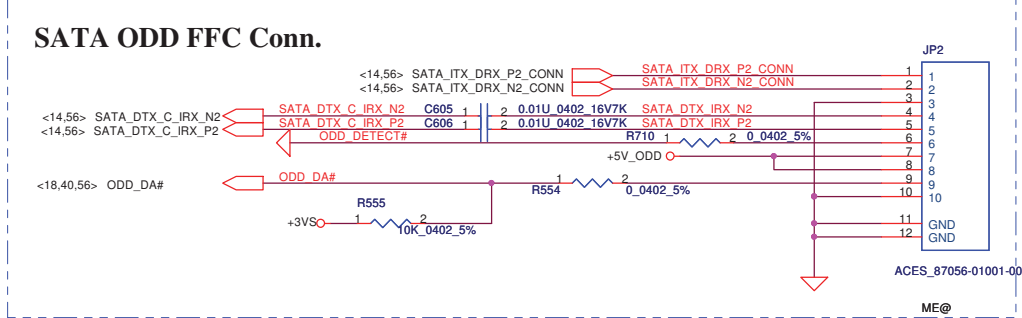
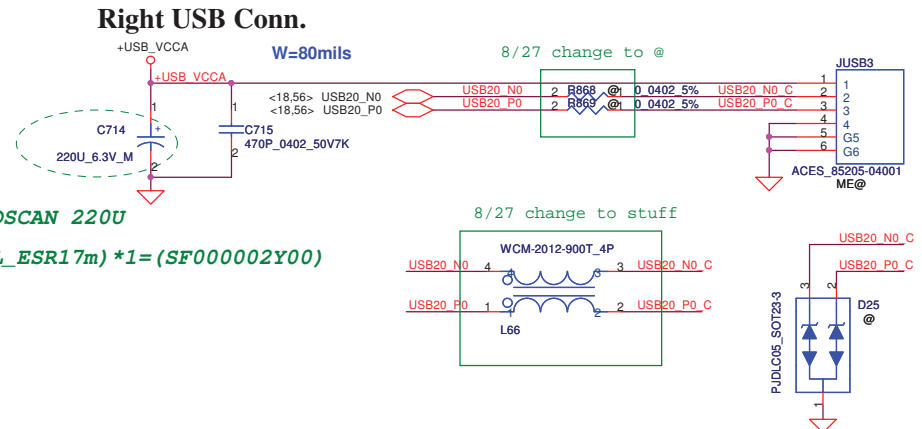
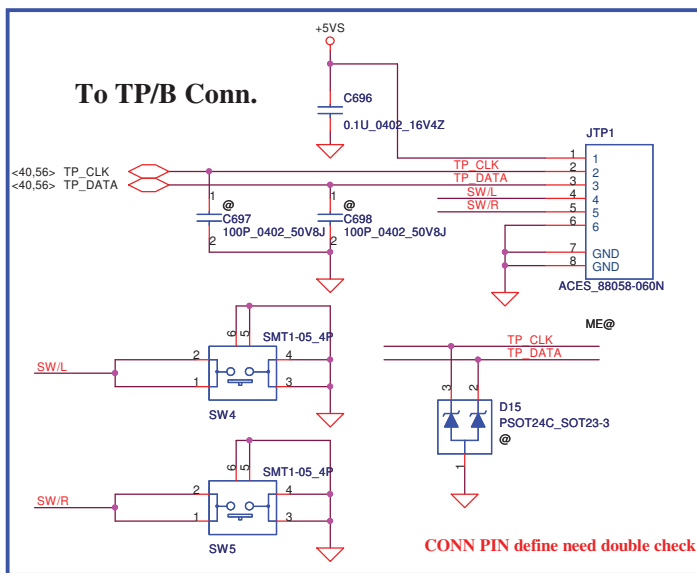
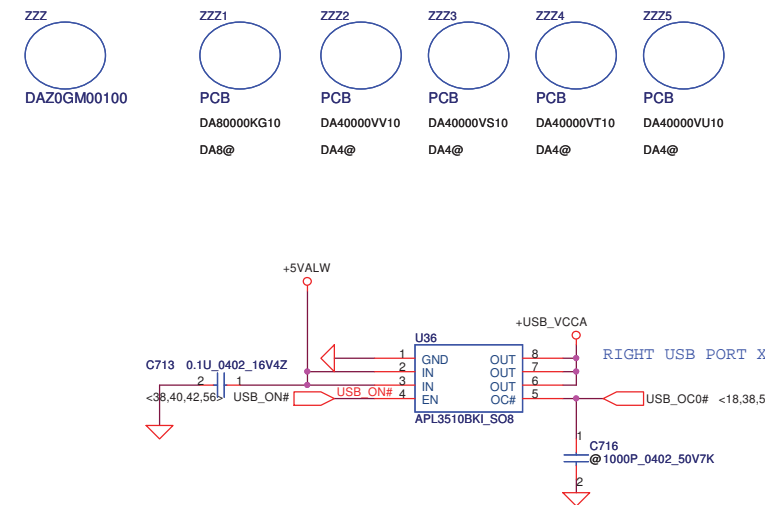
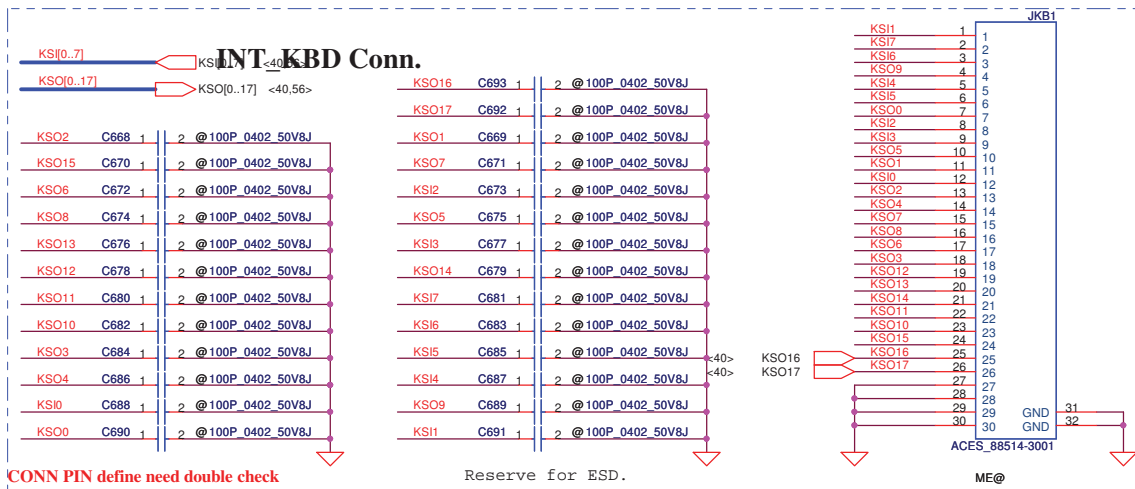
Security Classification	Compal Secret Data			<i>Compal Electronics, Inc.</i> Title PWR +CPU_CORE/+VGFX_CORE	
Issued Date	2010/01/25	Deciphered Date	2010/12/31	Size	Document Number
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				Date:	Friday, November 26, 2010 Sheet 53 of 54

Version change list (P.I.R. List)

Page 1 of 1
for PWR

Item	Reason for change	PG#	Modify List	Date	Phase
1	To reduce charger ripple	47	Add PC323	2010.08.15	DVT
2	HW request for power sequence	51	Change +VGA_PCIE enable signal from PX_MODE to PE_GPIO1 PR804:120K PR831,PR801,PR825 UN-POP PR824:47K PC819:0.2uF	2010.08.15	DVT
3	Change Vboot setting	52	Change PR942 as 4.32K	2010.08.15	DVT
4	Change OCP setting	52	Change PR958 as 1.47K	2010.08.15	DVT
5	Add PC955 for loadline adjust	52	Add PC955	2010.08.15	DVT
6	Reserve pull low resistor	51	Add PR718,PR832	2010.09.29	PVT
7	Remove jump	51	Remove PJ802,PJ803	2010.09.29	PVT
8	Adapter protect circuit	46	Pop PR222,PR208,PH202,PR221,PQ204 Un-Pop PR223,PR203	2010.09.29	PVT
9	EMI Request	47	Remove PJ301 Add PL302 and reserve PC324	2010.09.29	PVT
10					
11					
12					
13					
14					
15					
16					
17					

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				PIWG1/G2	
Date: Friday, November 26, 2010				Sheet	54 of 54



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PHASE	PAGE	Modification list	PURPOSE
0.3	P10	Update Q5 symbol	For update symbol
0.3	P33	Add F2	For safty request
0.3	P39	Update U30 P/N to SA00003K410 and Add R879	For Audio update to 21Z
0.3	P10	Change C128 to D2 size and @	Change size for M/E issue
0.3	P14	Add reserve R878	For Intel DG 1.5
0.3	P37	C592 change P/N to SF000001500 (H=6)	For ME Z high ok
0.3	P25	Update Q69-Q72 to A03414 ,D28 R873 change to BAC0@ , U40 change to @	For PX4.0
0.3	P28	Add reserve C94	For reserve VGA_CORE
0.3	P29	R369 P/N change to SD034100A80	For GP part
0.3	P18	R553,R691,R684,R682,U12 change to PX@	For PX 4.0
0.3	P6	Reserved R880 to SYS_PWROK	Follow ORB
0.3	P10	R62,R63 change to 1k	Follow CRB
0.3	P19	R303 change to @, Change M/B ID to PX4.0	For ESATA and PX4.0
0.3	P25	Q69-Q72 change to BAC0 @	For Px4.0
0.3	P26	R719 change to stuff, R744 change to @ , R677 change to BAC0@	For PX4.0
0.3	P33	R483,R484 change connect to +5V_HDMI_F	For Add F2
0.3	P37	Change U27 P/N to SA000046C00	For Fintek
0.3	P40	Change R594 pull high to +5VALW	For leakage issue
0.3	P19	R881 change to Dstuff, R244 change to @	For intel MRC Rev0.9
0.3	P14	R878 change to stuff	For intel DG 1.5
0.3	P31	Del R432	For non-used part
0.3	P36	Reserved D31 , C643 , C644	For reserved EMI parts
0.3	P37	Del R581	For non-used part
0.3	P38	Del R550	For non-used part
0.3	P38	Change C592 P/N to SF000002Y00	For M/E Z high limlt
0.3	P39	Del R584, R586 , R587	For non-used part
0.3	P40	Change R600, R604 to 2.2k Change R695 to 8.2k	Change R600, R604 for Battery SMBus, R695 for Board ID
0.3	P42	Del R583	For non-used part
0.3	P6	Reserved R882 connect to PCH_PWROK	Reserved for intel
0.3	P56	R765 change to 300 ohm	For LED
0.3	P25	R324, R744 , R674 change DIS@	For DIS only sku